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# A Semantics to Generate the Context-sensitive Synchronized Control-Flow Graph (extended)^ 

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#### Abstract

ProB is an automated analysis tool set that allows the animation and verification of complex systems specified with the CSP language. This tool has been successfully applied in different industrial projects for the verification of systems with many concurrent and synchronized components. However, the cost of the analyses performed by ProB is usually very high, and sometimes prohibitive, due to the complexity imposed by the non-deterministic execution order of processes and to the restrictions imposed on this order by synchronizations. To overcome this problem, there has been a recent proposal that allows to statically simplify a specification before the analyses. This simplification allows to drastically reduce the time needed by ProB's analyses. Unfortunately, the approach has been implemented but it has not been formalized neither proved correct. In this paper, we formally define the data structures needed to automatically simplify a CSP specification and we define an algorithm able to automatically generate these data structures. The algorithm has been proved correct and its implementation for ProB is publicly available.


## 1 Introduction

The Communicating Sequential Processes (CSP) [2,11] language allows us to specify complex systems with multiple interacting processes. The study and transformation of such systems often implies different analyses (e.g., deadlock analysis [4], reliability analysis [3], refinement checking [10], etc.) which are often based on a data structure able to represent all computations of a specification.

Recently, a new data structure called Context-sensitive Synchronized ControlFlow Graph (CSCFG) has been defined [6]. This data structure is a graph that allows us to finitely represent possibly infinite computations, and it is particularly interesting because it takes into account the context of process calls, and thus it allows us to produce analyses that are very precise. In particular, some analyses (see, e.g., $[7,8]$ ) use the CSCFG to simplify a specification with respect

[^0]to some term by discarding those parts of the specification that cannot be executed before the term and thus they cannot influence it. This simplification is automatic and thus it is very useful as a preprocessing stage of other analyses.

However, computing the CSCFG is a complex task due to the non-deterministic execution of processes, due to deadlocks, due to non-terminating processes and mainly due to synchronizations. This is the reason why there does not exist any correctness result which formally relates the CSCFG of a specification to its execution. This result is needed because it would allow us to prove important properties (such as correctness and completeness) of the techniques based on the CSCFG.

In this work, we instrument the CSP standard semantics (Chapter 7 in [11]) in such a way that the execution of the instrumented semantics produces as a side-effect the portion of the CSCFG which is associated to the performed computation. Then, we define an algorithm which uses the semantics to build the complete CSCFG associated to a CSP specification. This algorithm executes the semantics several times to explore all possible computations of the specification. Each computation produces a part of the CSCFG, and finally, the algorithm joins all the parts so that a complete CSCFG is produced.

The rest of the paper has been organized as follows. Section 2 recalls CSP syntax and presents its standard operational semantics as defined by A.W. Roscoe [11]. Section 3 introduces some notation that will be used along the paper and it formally defines the CSCFG. Section 4 presents an algorithm able to generate the CSCFG associated to a CSP specification. To obtain the CSCFG, the algorithm uses an instrumentation of the standard operational semantics of CSP which is also introduced in this section. In Section 5, two theorems are proved that ensure that the algorithm is correct and terminating. Finally, Section 6 concludes.

## 2 The syntax and semantics of CSP

In order to make the paper self-contained, this section recalls CSP's syntax and semantics.

Figure 1 summarizes the syntax constructions used in CSP specifications. A specification is a finite collection of definitions. The left-hand side of each definition is the name of a different process, which is defined in the right-hand side (abbrev. rhs) by means of an expression that can be a call to another process or a combination of the following operators:

Prefixing It specifies that event $a$ must happen before process $P$.
Input It is used to receive a message from another process. Message $a$ is received through channel $b$; then $P$ is executed.
Output It is analogous to the input, but this is used to send messages. Message $a$ is sent through channel $b$; then process $P$ is executed.
Internal choice The system (e.g., non-deterministically) chooses to execute one of the two processes $P$ or $Q$.

```
Domains
    M,N\ldots\in\mathcal{N}\mathrm{ (Process names)}
    P,Q\ldots\in\mathcal{P}\quad\mathrm{ (Processes)}
    a,b\ldots\in\Sigma (Events)
```

```
S::= D N ... Dm (Entire specification)
D::=N=P (Process definition)
P ::=M (Process call)
    a->P (Prefixing)
    | a?b }->P\mathrm{ (Input)
    | a! b->P (Output)
    | P}\sqcapQ (Internal choice)
    | P\squareQ (External choice)
    | P|| Q (Synchronized parallelism) X\subseteq\Sigma
    | P||| Q (Interleaving)
    | P;Q (Sequential composition)
    | P\X (Hiding) X\subseteq\Sigma
    | P\llbracketf\rrbracket (Renaming) f:\Sigma->\Sigma
    | SKIP (Skip)
    STOP (Stop)
```

Fig. 1. Syntax of CSP specifications

External choice It is identic to internal choice but the choice comes from outside the system (e.g., the user).
Synchronized parallelism Both processes are executed in parallel with a set $X$ of synchronized events. In absence of synchronizations both processes can execute in any order. Whenever a synchronized event $a \in X$ happens in one of the processes it must also happen in the other at the same time. Whenever the set of synchronized events is not specified, it is assumed that processes are synchronized in all common events. A particular case of parallel execution is interleaving where no synchronizations exist (i.e., $X=\emptyset$ ).
Interleaving Both expressions are executed in parallel and independently.
Sequential composition It specifies a sequence of two processes. When the first (successfully) finishes, the second starts.
Hiding Process $P$ is executed with a set of hidden events $X \subseteq \Sigma$. Hidden events are not observable from outside the process, and thus, they cannot synchronize with other processes.
Renaming Process $P$ is executed with a set of renamed events specified with the mapping $f$. An event $a$ renamed as $b$ behaves internally as $a$ but it is observable as $b$ from outside the process.
Skip It finishes the current process. It allows us to continue the next sequential process.
Stop Synonym of deadlock: It finishes the current process.

| (Process Call) | (Prefixing) |
| :---: | :---: |
| $N \xrightarrow{\tau} r h s(N)$ | $\overline{(a \rightarrow P) \xrightarrow{a} P}$ |
| (Internal Choice 1) | (Internal Choice 2) |
| $\overline{(P \sqcap Q) \xrightarrow{\tau} P}$ | $\overline{(P \sqcap Q) \xrightarrow{\tau} Q}$ |
| (External Choice 1) | (External Choice 2) |
| $P \xrightarrow{\tau} P^{\prime}$ | $Q \xrightarrow{\tau} Q^{\prime}$ |
| $\overline{(P \square Q) \xrightarrow{\tau}\left(P^{\prime} \square Q\right)}$ | $(P \square Q) \xrightarrow{\tau}\left(P \square Q^{\prime}\right)$ |
| (External Choice 3) | (External Choice 4) |
| $P \xrightarrow{e} P^{\prime}$ | $Q \xrightarrow{e} Q^{\prime}$ |
| $\overline{(P \square Q) \xrightarrow{e} P^{\prime}} \quad e \in \Sigma$ | $\overline{(P \square Q) \xrightarrow{e} Q^{\prime}} \quad e \in$ |
| (Synchronized Parallelism 1) | (Synchronized Parallelism 2) |
| $P \xrightarrow{e} P^{\prime}$ | $Q \xrightarrow{e} Q^{\prime}$ |
| $\left(P \\|_{X} Q\right) \xrightarrow{e}\left(P^{\prime} \\|_{X} Q\right)$ <br> (Synchronized Parallelism 3) | $\left(P \\|_{X} Q\right) \xrightarrow{e}\left(P \\|_{X} Q^{\prime}\right)$ <br> (Synchronized Parallelism 4) |
|  | $\overline{\left(\Omega \\|_{X} \Omega\right) \xrightarrow{\checkmark} \Omega}$ |
| (Sequential Composition 1) | (Sequential Composition 2) |
| $P \xrightarrow{e} P^{\prime}$ | $P \xrightarrow{\checkmark} \Omega$ |
| $(P ; Q) \xrightarrow{e}\left(P^{\prime} ; Q\right)$ | $(P ; Q) \xrightarrow{\tau} Q$ |
| (Hiding 1) | (Hiding 2) |
| $P \xrightarrow{e} P^{\prime}$ | $P \xrightarrow{v} P^{\prime}$ |
| $\overline{(P \backslash B) \xrightarrow{\tau}\left(P^{\prime} \backslash B\right)}$ 佼 | $\overline{(P \backslash B) \xrightarrow{v}\left(P^{\prime} \backslash B\right)}$ |
| (Hiding 3) | $(v=a \wedge a \notin B) \vee(v=\tau)$ |
| $P \xrightarrow{\checkmark} \Omega$ |  |
| $\xrightarrow{(P \backslash B) \xrightarrow{\checkmark} \Omega}$ |  |
| (Renaming 1) | (Renaming 2) |
| $P \xrightarrow{v^{\prime}} P^{\prime}$ | $P \xrightarrow{\checkmark} \Omega$ |
| $\overline{(P \llbracket R \rrbracket) \xrightarrow{v}\left(P^{\prime} \llbracket R \rrbracket\right)}$ | $(P \llbracket R \rrbracket) \xrightarrow{\checkmark} \Omega$ |
| $\begin{gathered} v=b \wedge v^{\prime}=a \wedge a R b \\ \\ \vee v=v^{\prime}=\tau \end{gathered}$ |  |
| (SKIP) |  |
| SKIP $\xrightarrow{\checkmark} \Omega$ |  |

Fig. 2. CSP's operational semantics

We now recall the standard operational semantics of CSP as defined by Roscoe [11]. It is presented in Fig. 2 as a logical inference system. A state
of the semantics is a process to be evaluated called the control. In the following, wlog we assume that the system starts with an initial state MAIN, and the rules of the semantics are used to infer how this state evolves. When no rules can be applied to the current state, the computation finishes. The rules of the semantics change the states of the computation due to the occurrence of events. The set of possible events is $\Sigma^{\tau}=\Sigma \cup\{\tau\}$. Events in $\Sigma$ are visible from the external environment, and can only happen with its co-operation (e.g., actions of the user). Event $\tau$ is an internal event that cannot be observed from outside the system and it happens automatically as defined by the semantics.

In order to perform computations, we construct an initial state (e.g., MAIN) and (non-deterministically) apply the rules of Fig. 2. The intuitive meaning of each rule is the following:
(Process Call) The call to process $N$ is unfolded and its right-hand side is added to the control.
(Prefixing) When event $a$ occurs, process $P$ is added to the control. This rule is used both for prefixing and communication operators (input and output). Given a communication expression, either $a ? b \rightarrow P$ or $a!b \rightarrow P$, this rule treats the expression as a prefixing except for the fact that the set of $b$ 's appearing in $P$ is replaced by the communicated events.
(SKIP) After SKIP, the only possible event is $\checkmark$, which denotes the successful termination of the (sub)computation with the special symbol $\Omega$. There is no rule for $\Omega$ (neither for STOP), hence, this (sub)computation has finished.
(Internal Choice 1 and 2) The system, with the occurrence of the internal event $\tau$, (non-deterministically) selects one of the two processes $P$ or $Q$ which is added to the control.
(External Choice 1, 2, 3 and 4) The occurrence of $\tau$ develops one of the branches. The occurrence of an event $e \in \Sigma$ is used to select one of the two processes $P$ or $Q$ and the control changes according to the event.
(Sequential Composition 1) In $P ; Q, P$ can evolve to $P^{\prime}$ with any event except $\checkmark$. Hence, the control becomes $P^{\prime} ; Q$.
(Sequential Composition 2) When $P$ finishes (with event $\checkmark$ ), $Q$ starts. Note that $\checkmark$ is hidden from outside the whole process becoming $\tau$.
(Synchronized Parallelism 1 and 2) When a non-synchronized event happens, one of the two processes $P$ or $Q$ evolves accordingly.
(Synchronized Parallelism 3) When a synchronized event ( $a \in X$ ) happens, it is required that both processes synchronize; $P$ and $Q$ are executed at the same time and the control becomes $P^{\prime} \|_{X} Q^{\prime}$.
(Synchronized Parallelism 4) When both processes have successfully terminated the control becomes $\Omega$, performing $\checkmark$.
(Sequential Composition 1) In $P ; Q, P$ can evolve to $P^{\prime}$ with any event except $\checkmark$. Hence, the control becomes $P^{\prime} ; Q$.
(Sequential Composition 2) When $P$ finishes (with event $\checkmark$ ), $Q$ starts. Note that $\checkmark$ is hidden from outside the whole process becoming $\tau$.
(Hiding 1) When event $a \in B$ occurs in $P$, it is hidden, and thus changed to $\tau$ so that it is not observable from outside $P$.
(Hiding 2 and Hiding 3) $P$ can normally evolve (using rule 2) until it is finished ( $\checkmark$ happens). When $P$ finishes, the control becomes $\Omega$.
(Renaming 1 and 2) Whenever a renamed event $a$ happens in $P$, it is renamed to $b$ so that, externally, only $b$ is visible. Renaming has no effect on either $\tau$ or $\checkmark$ events.

## 3 Context-sensitive Synchronized Control-Flow Graphs

A CSCFG $[6,8]$ is a data structure able to finitely represent all possible (often infinite) computations of a CSP specification. This data structure is particularly useful to simplify a CSP specification before its static analysis. The simplification of industrial CSP specifications allows to drastically reduce the time needed to perform expensive analyses such as model checking. Algorithms to construct CSCFGs have been implemented [7] and integrated into the most advanced CSP environment ProB [5]. In this section we present a formalization of the CSCFG which directly relates the graph construction with the control-flow of the computations it represents.

A CSCFG is formed by the sequence of expressions that are evaluated during an execution. These expressions are conveniently connected to form a graph. In addition, the source position (in the specification) of each literal is also included in the CSCFG. This is very useful because it provides the CSCFG with the ability to determine what parts of the source code have been executed and in what order. The inclusion of source positions in the CSCFG implies an additional level of complexity in the semantics, but the benefits of providing the CSCFG with this additional information are clear and, for some applications, essential. Therefore, we use labels (that we call specification positions) to identify each literal in a specification (e.g., events, operators and process names). Formally,

Definition 1. (Specification position) $A$ specification position is a pair ( $N, w$ ) where $N \in \mathcal{N}$ and $w$ is a sequence of natural numbers (we use $\Lambda$ to denote the empty sequence). We let $\mathcal{P o s}(P)$ denote the specification position of a CSP process $P$. Each process definition $N=P$ of a CSP specification is labeled with specification positions with the call $\operatorname{AddSpPos}(P,(N, \Lambda))$; where function $\operatorname{AddSpPos}$ is defined as follows:

$$
\begin{aligned}
& \operatorname{AddSpPos}(P,(N, w))= \\
& \begin{cases}P_{(N, w)} & \text { if } P \in \mathcal{N} \\
S K I P_{(N, w)} & \text { if } P=S K I P \\
S T O P_{(N, w)} & \text { if } P=S T O P \\
a_{(N, w .1)} \rightarrow(N, w) \operatorname{AddSpPos}(Q,(N, w .2)) & \text { if } P=a \rightarrow Q \\
\operatorname{AddSpPos}(Q,(N, w .1)) o p & \text { if } P=Q \text { op } \quad \forall o p \in\{\backslash, \llbracket \rrbracket\} \\
\operatorname{AddSpPos}(Q,(N, w .1)) o p_{(N, w)} \operatorname{AddSpPos}(R,(N, w .2)) \\
r & \text { if } P=Q \text { op } R \quad \forall o p \in\{\sqcap, \square,\|,\| \|, ;\}\end{cases}
\end{aligned}
$$

We refer to a CSP process $P$ with the specification position of its root, i.e., $\mathcal{P o s}(a \rightarrow Q)=\mathcal{P} o s(\rightarrow)$ and $\mathcal{P} o s(Q$ op $R)=\mathcal{P o s}(o p) \forall$ op $\in\{\sqcap, \square,\|, \mid\|, ;\}$. We often use $\operatorname{Pos}(\mathcal{S})$ to denote a set containing all positions in a specification $\mathcal{S}$.

Example 1. Consider the following CSP specification where literals are labeled with its associated specification positions (in grey color) so that labels are unique:

$$
\begin{aligned}
& \left(\mathrm{P}_{(\text {main, 2.1) }} \square_{(\text {main, } 2)}\left(\mathbf{a}_{(\text {main, 2.2.1 }} \rightarrow{ }_{(\text {main, 2.2 })} \operatorname{STOP}_{(\text {main, 2.2.2) }}\right)\right) \\
& \mathrm{P}=\mathrm{b}_{(\mathrm{P}, 1)} \rightarrow(\mathrm{P}, \Lambda) \operatorname{STOP}_{(\mathrm{P}, 2)}
\end{aligned}
$$

Note that the specification positions of each process can be viewed as a tree whose root is $\Lambda$. We often use indistinguishably an expression and its associated specification position, when it is clear from the context (e.g., in Example 1 we will refer to ( $\mathrm{P}, 1$ ) as b).

In order to introduce the definition of CSCFG, we need first to define the concepts of control-flow, path and context.

Definition 2. (Control-flow) Given a CSP specification $\mathcal{S}$, the control-flow is a transitive relation between the specification positions of $\mathcal{S}$. Given two specification positions $\alpha, \beta$ in $\mathcal{S}$, we say that the control of $\alpha$ can pass to $\beta$ iff
i) $\alpha=N \wedge \beta=\operatorname{first}((N, \Lambda))$ with $N=\operatorname{rhs}(N) \in \mathcal{S}$
ii) $\alpha \in\{\sqcap, \square, \|\} \wedge \beta \in\{\operatorname{first}(\alpha .1)$, $\operatorname{first}(\alpha .2)\}$
iii) $\alpha \in\{\rightarrow, ;\} \wedge \beta=\operatorname{first}(\alpha .2)$
iv) $\alpha=\beta .1 \wedge \beta=\rightarrow$
v) $\alpha \in \operatorname{last}(\beta .1) \wedge \beta=$;
vi) $\alpha \in\{\backslash, \llbracket \mathbb{1}\} \wedge \beta=\operatorname{first}(\alpha .1)$
where first $(\alpha)$ is defined as follows:

$$
\operatorname{first}(\alpha)= \begin{cases}\alpha .1 & \text { if } \alpha=\rightarrow \\ \operatorname{first}(\alpha .1) & \text { if } \alpha=; \\ \alpha & \text { otherwise }\end{cases}
$$

and where last $(\alpha)$ is the set of all possible termination points of the subprocess denoted by $\alpha$ :

$$
\operatorname{last}(\alpha)= \begin{cases}\{\alpha\} & \text { if } \alpha=\text { SKIP } \\ \emptyset & \text { if } \alpha=\operatorname{STOP} \vee \\ & (\alpha \in\{\|\} \wedge(\operatorname{last}(\alpha .1)=\emptyset \vee \operatorname{last}(\alpha .2)=\emptyset)) \\ \operatorname{last}(\alpha .1) \cup \operatorname{last}(\alpha .2) & \text { if } \alpha \in\{\sqcap, \square\} \vee \\ & (\alpha \in\{\| \mid\} \wedge \operatorname{last}(\alpha .1) \neq \emptyset \wedge \operatorname{last}(\alpha .2) \neq \emptyset) \\ \operatorname{last}(\alpha .2) & \text { if } \alpha \in\{\rightarrow, ;\} \\ \operatorname{last}((N, \Lambda)) & \text { if } \alpha=N\end{cases}
$$

We say that a specification position $\alpha$ is executable in $\mathcal{S}$ iff the control can pass from the initial state (i.e., MAIN) to $\alpha$.

For instance, in Example 1, the control can pass from (MAIN, 2.1) to (P, 1) due to rule i), from (MAIN, 2) to (MAIN, 2.1) and (MAIN, 2.2.1) due to rule ii), from (MAIN, 2.2.1) to (MAIN, 2.2) due to rule iii), and from (MAIN, 2.2) to (MAIN, 2.2.2) due to rule iv).

As we will work with graphs whose nodes are labeled with positions, we use $l(n)$ to refer to the label of node $n$.

Definition 3. (Path) Given a labeled graph $\mathcal{G}=(N, E)$, a path between two nodes $n_{1}, m \in N, \operatorname{Path}\left(n_{1}, m\right)$, is a sequence $n_{1}, \ldots, n_{k}$ such that $n_{k} \mapsto m \in E$ and for all $1 \leq i<k$ we have $n_{i} \mapsto n_{i+1} \in E$. The path is loop-free if for all $i \neq j$ we have $n_{i} \neq n_{j}$.

Definition 4. (Context) Given a labeled graph $\mathcal{G}=(N, E)$ and a node $n \in N$, the context of $n$, $\mathcal{C o n}(n)=\{m \mid l(m)=M$ with $(M=P) \in \mathcal{S}$ and exists a loop-free path $m \mapsto^{*} n$ with last $\left.(m) \notin \pi\right\}$.

Intuitively speaking, the context of a node represents the set of processes in which a particular node is being executed. This is represented by the set of process calls in the computation that were done before the specified node. For instance, the CSCFG associated to the specification in Example 1 is shown in Fig. 3. In this graph we have that $\mathcal{C}$ on $(4)=\{0,3\}$, i.e., b is being executed after having called processes MAIN and P. Note that focussing on a process call node we can use the context to identify loops; i.e., we have a loop whenever $n \in \mathcal{C}$ on $(m)$ with $l(n)=l(m)=M$.

Definition 5. (Context-sensitive Synchronized Control-Flow Graph) Given a CSP specification $\mathcal{S}$, its Context-sensitive Synchronized Control-Flow Graph $(C S C F G)$ is a labeled directed graph $\mathcal{G}=\left(N, E_{c}, E_{l}, E_{s}\right)$ where $N$ is a set of nodes such that $\forall n \in N . l(n) \in \mathcal{P o s}(\mathcal{S})$ and $l(n)$ is executable in $\mathcal{S}$; and edges are divided into three groups: control-flow edges ( $E_{c}$ ), loop edges ( $E_{l}$ ) and synchronization edges ( $E_{s}$ ).
$-E_{c}$ is a set of one-way edges (denoted with $\mapsto$ ) representing the possible control-flow between two nodes. Control edges do not form loops. The root of the tree formed by $E_{c}$ is the position of the initial call to MAIN.

- $E_{l}$ is a set of one-way edges (denoted with $\left.\rightsquigarrow\right)$ such that $\left(n_{1} \rightsquigarrow n_{2}\right) \in E_{l}$ iff $l\left(n_{1}\right)$ and $l\left(n_{2}\right)$ are (possibly different) process calls that refer to the same process $M \in \mathcal{N}$ and $n_{2} \in \mathcal{C o n}\left(n_{1}\right)$.
- $E_{s}$ is a set of two-way edges (denoted with $\leftrightarrow$ ) representing the possible synchronization of two (event) nodes.

Given a CSCFG, every node labeled $(M, \Lambda)$ has one and only one incoming edge in $E_{c}$; and every process call node has one and only one outgoing edge which belongs to either $E_{c}$ or $E_{l}$.

Example 2. Consider again the specification of Example 1 and its associated CSCFG shown in Fig. 3. For the time being, the reader can ignore the numbering


Fig. 3. CSCFG associated to the specification of Example 1
and color of the nodes; they will be explained in Section 4. Each process call is connected to a subgraph which contains the right-hand side of the called process. For convenience, in this example there are no loop edges; there are control-flow edges and one synchronization edge between nodes (MAIN, 2.2.1) and (MAIN, 1.1) representing the synchronization of event a.

Note that the CSCFG shows the exact expressions that have been evaluated with an explicit causality relation; and, in addition, it shows the specification positions that have been evaluated and in what order. Therefore, it is not only useful as a program comprehension tool, but it can be used for program simplification. For instance, with a simple backwards traversal from a, the CSCFG of Fig. 3 reveals that the only part of the code that can be executed before a is the black part:

$$
\begin{gathered}
\text { MAIN }=(\mathrm{a} \rightarrow \text { STOP }) \|(\mathrm{P} \square(\mathrm{a} \rightarrow \text { STOP })) \\
\mathrm{P}=\mathrm{b} \xrightarrow{\{\mathrm{a}\}} \operatorname{STOP}
\end{gathered}
$$

Hence, the specification can be significantly simplified for those analyses focussing on the occurrence of event a.

## 4 An algorithm to generate the CSCFG

This section introduces an algorithm able to generate the CSCFG associated to a CSP specification. The algorithm uses an instrumented operational semantics
of CSP which (i) generates as a side-effect the CSCFG associated to the computation performed with the semantics; (ii) it controls that no infinite loops are executed; and (iii) it ensures that the execution is deterministic.

Algorithm 1 controls that the semantics is executed repeatedly in order to deterministically execute all possible computations - of the original (nondeterministic) specification-and then, it joins all the graphs produced for each computation in order to construct a complete CSCFG for the whole specification. The key point of the algorithm is the use of a stack that records the actions that can be performed by the semantics. In particular, the stack contains tuples of the form (rule, rules) where rule indicates the rule that must be selected by the semantics in the next execution step, and rules is a set with the other possible rules that can be selected. The algorithm uses the stack to prepare each execution of the semantics indicating the rules that must be applied at each step. For this, function UpdStack is used; it basically avoids to repeat the same computation with the semantics. When the semantics finishes, the algorithm adds to $G$ the set of synchronizations occurred (those in $\zeta$ ), and it prepares a new execution of the semantics with an updated stack. This is repeated until all possible computations are explored (i.e., until the stack is empty).

The standard operational semantics of CSP [11] can be non-terminating due to infinite computations. However, the CSCFG is finite, and thus, we must ensure that the algorithm to compute it always terminates. Therefore, the instrumentation of the semantics incorporates a loop-checking mechanism to ensure termination.

```
Algorithm 1 General Algorithm
    Build the initial state of the semantics:
    state \(=\left(\operatorname{MAIN}_{(\text {main }, 0)}, \emptyset, \bullet,(\emptyset, \emptyset), \emptyset, \emptyset\right)\)
    repeat
        repeat
            Run the rules of the semantics with the state state
        until no more rules can be applied
        Get the new state state \(=\left(-, G,,\left(\emptyset, S_{0}\right),-, \zeta\right)\)
        \(\forall(m \leftrightarrow n) \in \zeta . G[m \leftrightarrow n]\)
        state \(=\left(\operatorname{MAIN}_{(\operatorname{Main}, 0)}, G, \bullet,\left(\operatorname{UpdStack}\left(S_{0}\right), \emptyset\right), \emptyset, \emptyset\right)\)
    until UpdStack \(\left(S_{0}\right)=\emptyset\)
    return \(G\)
    where function UpdStack is defined as follows:
    \(\operatorname{UpdStack}(S)= \begin{cases}(\text { rule, rules } \backslash\{\text { rule }\}): S^{\prime} \text { if } S=(-, \text { rules }): S^{\prime} \\ \text { and rule } \in \text { rules } \\ \text { UpdStack }\left(S^{\prime}\right) & \text { if } S=(-, \emptyset): S^{\prime} \\ \emptyset & \text { if } S=\emptyset\end{cases}\)
```

The instrumented semantics used by Algorithm 1 is shown in Fig. 5. It is an operational semantics where we assume that every literal in the specification has been labeled with its specification position (denoted by a subscript, e.g.,
$\left.P_{\alpha}\right)$. In this semantics, a state is a tuple $\left(P, G, m,\left(S, S_{0}\right), \Delta, \zeta\right)$, where $P$ is the process to be evaluated (the control), $G$ is a directed graph (i.e., the CSCFG constructed so far), $m$ is a numeric reference to the current node in $G,\left(S, S_{0}\right)$ is a tuple with two stacks (where the empty stack is denoted by $\emptyset$ ) that contains the rules to apply and the rules applied so far, $\Delta$ is a set of references used to draw synchronizations in $G$ and $\zeta$ is a set which contains the synchronizations performed in each execution of the semantics, and it is used to detect loops. The basic idea of the graph construction is to record the current control with a fresh reference ${ }^{1} n$ by connecting it to its parent $m$. We use the notation $G[n \stackrel{m}{\mapsto} \alpha]$ either to introduce a node in $G$ or as a condition on $G$ (i.e., $G$ contains node $n$ ). This node has reference $n$, is labeled with specification position $\alpha$ and its parent is $m$. The edge introduced can be a control, a synchronization or a loop edge. This notation is very convenient because it allows to add nodes to $G$, but also to extract information from $G$. For instance, $G[3 \stackrel{m}{\mapsto} \alpha]$ allows to know the parent of node 3 (the value of $m$ ), and the specification position of node 3 (the value of $\alpha)$.

Note that the initial state for the semantics used by Algorithm 1 has MAIN(Main,0) in the control. This initial call to MAIN does not appear in the specification, thus we label it with a special specification position (MAIN, 0 ) which is the root of the CSCFG (see Fig. 3). Note that we use • as a reference in the initial state. The first node added to the CSCFG (i.e., the root) will have parent reference $\bullet$. Therefore, here - denotes the empty reference because the root of the CSCFG has no parent.

An explanation for each rule of the semantics follows:
(Process Call) The called process $N$ is unfolded, node $n$ (a fresh reference) is added to the graph with specification position $\alpha$ and parent $m$. In the new state, $n$ represents the current reference. The new expression in the control is $P^{\prime}$, computed with function LoopCheck which is used to prevent infinite unfolding and is defined below. No event can synchronize in this rule, thus $\Delta$ is empty and $\zeta$ does not change.

$$
\operatorname{LoopCheck}(N, n, G)= \begin{cases}\left(\circlearrowleft_{s}(r h s(N)), G[n \rightsquigarrow s]\right) & \text { if } \exists s \cdot s \stackrel{t}{\mapsto} N \in G \\ & \wedge s \in \operatorname{Path}(0, n) \\ (r h s(N), G) & \text { otherwise }\end{cases}
$$

Function LoopCheck checks whether the process call in the control has not been already executed (if so, we are in a loop). When a loop is detected, the righthand side of the called process is labeled with a special symbol $\circlearrowleft_{s}$ and a loop edge between nodes $n$ and $s$ is added to the graph. The loop symbol $\circlearrowleft$ is labeled with the position $s$ of the process call of the loop. This label is later used by rule (Synchronized Parallelism 4) to decide whether the process must be stopped. It is also used to know what is the reference of the process' node if it is unfolded again.
(Prefixing) This rule adds nodes $n$ (the prefix) and $o$ (the prefixing operator) to the graph. In the new state, o becomes the current reference. The new control

[^1]

Fig. 4. An instrumented operational semantics that generates the CSCFG
is $P$. The set $\Delta$ is $\{n\}$ to indicate that event $a$ has occurred and it must be

| (Synchronized Parallelism 4) |
| :---: |
| $\begin{aligned} & \left(P 1 \\|_{X}^{\left(\alpha, n_{1}, n_{2}, r\right)} P 2, G, m,\left(S^{\prime}:(\mathrm{SP} 4, \text { rules }), S_{0}\right),,-\zeta\right) \xrightarrow{\tau}\left(P^{\prime}, G, m,\left(S^{\prime \prime}, S_{0}^{\prime}\right), \emptyset, \zeta\right) \\ & P^{\prime}=\operatorname{LoopControl}\left(P 1 \\|_{X}(\alpha, n 1, n 2, \Upsilon) P 2, m\right) \end{aligned}$ <br> (Synchronized Parallelism 5) $\left.\xrightarrow[X]{\left(P 1 \\|_{X}\left(\alpha, n_{1}, n_{2}, r\right)\right.} P 2, G, m,\left([(\text { rule, rules })], S_{0}\right),-, \zeta\right) \xrightarrow{e}\left(P, G^{\prime}, m,\left(S^{\prime}, S_{0}^{\prime}\right), \Delta, \zeta^{\prime}\right)$ |
|  |  |
|  |
| (STOP) |
| $\left(S T O P_{\alpha}, G, m,\left(S, S_{0}\right),{ }_{-}, \zeta\right) \xrightarrow{\tau}\left(\perp, G[n \stackrel{m}{\mapsto} \alpha], n,\left(S, S_{0}\right), \emptyset, \zeta\right)$ |

Fig. 5. An instrumented operational semantics that generates the CSCFG (cont.)
synchronized when required by (Synchronized Parallelism 3). The set $\zeta$ does not change.
(Choice) The only sources of non-determinism are choice operators (different branches can be selected for execution) and parallel operators (different order of branches can be selected for execution). Therefore, every time the semantics executes a choice or a parallelism, they are made deterministic thanks to the information in the stack $S$. In the case of choices (both internal and external can be treated with a single rule), this rule adds node $n$ to the graph which is labeled with the specification position $\alpha$ and has parent $m$. In the new state, $n$ becomes the current reference. No event can synchronize in this rule, thus $\Delta$ is empty and $\zeta$ does not change.

Function SelectBranch is used to produce the new control $P^{\prime}$ and the new tuple of stacks $\left(S^{\prime}, S_{0}^{\prime}\right)$, by selecting a branch with the information of the stack. Note that, for simplicity, the lists constructor ":" has been overloaded, and it is also used to build lists of the form $(A: a)$ where $A$ is a list and $a$ is the last element:

SelectBranch $\left(P \sqcap_{\alpha} Q,\left(S, S_{0}\right)\right)= \begin{cases}\left(P,\left(S^{\prime},(\mathrm{C} 1,\{\mathrm{C} 2\}): S_{0}\right)\right) & \text { if } S=S^{\prime}:(\mathrm{C} 1,\{\mathrm{C} 2\}) \\ \left(Q,\left(S^{\prime},(\mathrm{C} 2, \emptyset): S_{0}\right)\right) & \text { if } S=S^{\prime}:(\mathrm{C} 2, \emptyset) \\ \left(P,\left(\emptyset,(\mathrm{C} 1,\{\mathrm{C} 2\}): S_{0}\right)\right) & \text { otherwise }\end{cases}$
If the last element of the stack $S$ indicates that the first branch of the choice (C1) must be selected, then $P$ is the new control. If the second branch must be selected (C2), the new control is $Q$. In any other case the stack is empty, and thus this is the first time that this choice is evaluated. Then, we select the first branch ( $P$ is the new control) and we add ( $\mathrm{C} 1,\{\mathrm{C} 2\}$ ) to the stack $S_{0}$ indicating that C 1 has been fired, and the remaining option is C 2 .

For instance, when the CSCFG of Fig. 3 is being constructed and we reach the choice operator (i.e., (MAIN, 2)), then the left branch of the choice is evaluated and ( $\mathrm{C} 1,\{\mathrm{C} 2\}$ ) is added to the stack to indicate that the left branch has been evaluated. The second time it is evaluated, the stack is updated to $(C 2, \emptyset)$ and the

```
(Sequential Composition 1)
\(\frac{\left(P, G, m,\left(S, S_{0}\right), \emptyset, \zeta\right) \xrightarrow{e}\left(P^{\prime}, G^{\prime}, n,\left(S^{\prime}, S_{0}^{\prime}\right), \Delta, \zeta\right)}{\left(P ; Q, G, m,\left(S, S_{0}\right),-, \zeta\right) \xrightarrow{e}\left(P^{\prime \prime}, G^{\prime}, n,\left(S^{\prime}, S_{0}^{\prime}\right), \Delta, \zeta\right)} e \in \Sigma^{\tau}\)
\(P^{\prime \prime}=\circlearrowleft_{\varphi}\left(P_{\circlearrowleft}^{\prime} ; Q\right) \quad\) if \(P^{\prime}=\circlearrowleft\left(P_{\circlearrowleft}^{\prime}\right)\)
\(P^{\prime \prime}=P^{\prime} ; Q \quad\) otherwise
(Sequential Composition 2)
\(\frac{\left(P, G, m,\left(S, S_{0}\right), \emptyset, \zeta\right) \xrightarrow{\checkmark}\left(\Omega, G^{\prime}, q,\left(S, S_{0}\right), \emptyset, \zeta\right)}{\left(P ;_{\alpha} Q, G, m,\left(S, S_{0}\right),-, \zeta\right) \xrightarrow{\tau}\left(Q, G^{\prime}[q \stackrel{p}{\mapsto} \alpha], r,\left(S, S_{0}\right), \emptyset, \zeta\right)}\)
(Hiding 1)
\(\frac{\left(P, G_{\Pi}, m_{\Pi},\left(S, S_{0}\right), \emptyset, \zeta\right) \xrightarrow{a}\left(P^{\prime}, G^{\prime}, n,\left(S, S_{0}\right),-, \zeta\right)}{\left(P \backslash_{\alpha} B, G, m,\left(S, S_{0}\right),-, \zeta\right) \xrightarrow{\tau}\left(P^{\prime} \backslash \bullet B, G^{\prime}, n,\left(S, S_{0}\right), \emptyset, \zeta\right)} \quad a \in B\)
(Hiding 2)
\(\frac{\left(P, G_{\Pi}, m_{\Pi},\left(S, S_{0}\right), \emptyset, \zeta\right) \xrightarrow{e}\left(P^{\prime}, G^{\prime}, n,\left(S^{\prime}, S_{0}^{\prime}\right), \Delta, \zeta\right)}{\left(P \backslash_{\alpha} B, G, m,\left(S, S_{0}\right),-, \zeta\right) \xrightarrow{e}\left(P^{\prime \prime}, G^{\prime}, n,\left(S^{\prime}, S_{0}^{\prime}\right), \Delta, \zeta\right)} e \notin B \wedge e \in \Sigma^{\tau}\)
\(P^{\prime \prime}=\circlearrowleft_{\varphi}\left(P_{\circlearrowleft}^{\prime} \backslash \bullet B\right) \quad\) if \(P^{\prime}=\circlearrowleft\left(P_{\circlearrowleft}^{\prime}\right)\)
\(P^{\prime \prime}=P^{\prime} \backslash \cdot B \quad\) otherwise
(Hiding 3)
\(\frac{\left(P, G_{\Pi}, m_{\Pi},\left(S, S_{0}\right), \emptyset, \zeta\right) \xrightarrow{\checkmark}\left(\Omega, G^{\prime}, n,\left(S, S_{0}\right), \emptyset, \zeta\right)}{\left(P \backslash_{\alpha} B, G, m,\left(S, S_{0}\right),-, \zeta\right) \xrightarrow{\checkmark}\left(\Omega, G^{\prime}, n,\left(S, S_{0}\right), \emptyset, \zeta\right)}\) where \(\left(G_{\Pi}, m_{\Pi}\right)=\Pi(G, \alpha, m)\)
(Renaming 1)
\(\frac{\left(P, G_{\Pi}, m_{\Pi},\left(S, S_{0}\right), \emptyset, \zeta\right)^{\text {a or }}{ }^{\tau}\left(P^{\prime}, G^{\prime}, n,\left(S^{\prime}, S_{0}^{\prime}\right), \Delta, \zeta\right)}{\left(P \llbracket R \rrbracket_{\alpha}, G, m,\left(S, S_{0}\right),-, \zeta\right)^{b} \xrightarrow{\text { or }}^{\tau}\left(P^{\prime \prime}, G^{\prime}, n,\left(S^{\prime}, S_{0}^{\prime}\right), \Delta, \zeta\right)} a R b\)
\(P^{\prime \prime}=\circlearrowleft_{\varphi}\left(P_{\circlearrowleft}^{\prime} \llbracket R \rrbracket \bullet\right) \quad\) if \(P^{\prime}=\circlearrowleft\left(P_{\circlearrowleft}^{\prime}\right)\)
\(P^{\prime \prime}=P^{\prime} \llbracket R \rrbracket\) • otherwise
(Renaming 2)
\(\frac{\left(P, G_{\Pi}, m_{\Pi},\left(S, S_{0}\right), \emptyset, \zeta\right) \xrightarrow{\checkmark}\left(\Omega, G^{\prime}, n,\left(S, S_{0}\right), \emptyset, \zeta\right)}{\left(P \llbracket R \rrbracket_{\alpha}, G, m,\left(S, S_{0}\right),{ }_{-}, \zeta\right) \xrightarrow{\checkmark}\left(\Omega, G^{\prime}, n,\left(S, S_{0}\right), \emptyset, \zeta\right)}\) where \(\left(G_{\Pi}, m_{\Pi}\right)=\Pi(G, \alpha, m)\)
(SKIP)
\(\left(S K I P_{\alpha}, G, m,\left(S, S_{0}\right),-, \zeta\right) \xrightarrow{\checkmark}\left(\Omega, G[n \stackrel{m}{\longmapsto} \alpha], n,\left(S, S_{0}\right), \emptyset, \zeta\right)\)
```

Fig. 6. An instrumented operational semantics that generates the CSCFG (cont.)
right branch is evaluated. Therefore, the selection of branches is predetermined by the stack, thus, Algorithm 1 can decide what branches are evaluated by conveniently handling the information of the stack.
(Synchronized Parallelism 1 and 2) The stack determines what rule to use when a parallelism operator is in the control. If the last element in the stack is SP1, then (Synchronized Parallelism 1) is used. If it is SP2, (Synchronized Parallelism 2) is used.

In a parallelism, both parallel processes can be intertwiningly executed until a synchronized event is found. Therefore, nodes for both processes can be added
interweaved to the graph. Hence, the semantics needs to know in every state the references to be used in both branches. This is done by labeling parallelism operators with a tuple of the form $\left(\alpha, n_{1}, n_{2}, \Upsilon\right)$ where $\alpha$ is the specification position of the parallelism operator; $n_{1}$ and $n_{2}$ are respectively the references of the last node introduced in the left and right branches of the parallelism, and they are initialized to $\bullet$; and $\Upsilon$ is a node reference used to decide when to unfold a process call (in order to avoid infinite loops), also initialized to $\bullet$. The sets $\Delta$ and $\zeta$ are passed down unchanged so that another rule can use them if necessary. These rules develop the branches of the parallelism until they are finished or until they must synchronize. They use function InitBranch to introduce the parallelism into the graph the first time it is executed and only if it has not been introduced in a previous computation. For instance, consider the State 1 of Fig. 8 , where the parallelism operator is labeled with $(($ MAIN,$\Lambda), \bullet, \bullet, \bullet)$. Therefore, it is evaluated for the first time, and thus, when rule (Synchronized Parallelism 2) is applied, node $1 \stackrel{0}{\mapsto}(\operatorname{MAIN}, \Lambda)$, which refers to the parallelism operator, is added to $G$. After executing function InitBranch, we get a new graph and a new reference. Its definition is the following:
$\operatorname{InitBranch}(G, n, m, \alpha)= \begin{cases}(G[o \stackrel{m}{\rightarrow} \alpha], o) & \text { if } n=\bullet \\ (G, n) & \text { otherwise }\end{cases}$
(Synchronized Parallelism 3) It is applied when the last element in the stack is SP3. It is used to synchronize the parallel processes. In this rule, $\Upsilon$ is replaced by
$\bullet$, meaning that a synchronization edge has been drawn and the loops could be unfolded again if it is needed. All the events that have been executed in this step must be synchronized. Therefore, all the events occurred in the subderivations of $P 1\left(\Delta_{1}\right)$ and $P 2\left(\Delta_{2}\right)$ are mutually synchronized. In the case that all the synchronizations occurred in the step are already in $\zeta^{\prime \prime}$, this rule detects that the parallelism is in a loop; and thus, in the new control the parallelism operator is labeled with $\circlearrowleft$ and all the other loop labels are removed from it. This is done by a trivial function Unloop. The set $\zeta$ is only changed by this rule. The rest of the rules has no influence on it, but, contrarily, this rule adds to $\zeta$ all the synchronizations that happen with the occurrence of synchronized event $e$.
(Synchronized Parallelism 4) This rule is applied when the last element in the stack is SP4. It is used when none of the parallel processes can proceed (because they already finished, deadlocked or were labeled with $\circlearrowleft)$. When a process is labeled as a loop with $\circlearrowleft$, it can be unlabeled to unfold it once ${ }^{2}$ in order to allow the other processes to continue. This happens when the looped process is in parallel with other process and the later is waiting to synchronize with the former. In order to perform the synchronization, both processes must continue, thus the loop is unlabeled. Hence, the system must stop only when both parallel processes are marked as a loop. This task is done by function LoopControl. It

[^2]decides if the branches of the parallelism should be further unfolded or they should be stopped (e.g., due to a deadlock or an infinite loop):

LoopControl $\left(P \|_{X}(\alpha, p, q, \Upsilon) Q, m\right)=\left\{\begin{array}{c}\circlearrowleft_{m}\left(P_{\circlearrowleft}^{\prime} \|_{X}\left(\alpha, p_{\circlearrowleft}, q_{\circlearrowleft}, \bullet\right) Q_{\circlearrowleft}^{\prime}\right) \\ \text { if } P^{\prime}=\circlearrowleft_{p_{\circlearrowleft}}\left(P_{\circlearrowleft}^{\prime}\right) \wedge Q^{\prime}=\circlearrowleft_{q_{\circlearrowleft}}\left(Q_{\circlearrowleft}^{\prime}\right) \\ \circlearrowleft_{m}\left(P_{\circlearrowleft}^{\prime} \|_{X}\left(\alpha, p_{\circlearrowleft}, q^{\prime}, \bullet\right) \perp\right) \\ \text { if } P^{\prime}=\circlearrowleft_{p_{\circlearrowleft}}\left(P_{\circlearrowleft}^{\prime}\right) \\ \wedge\left(Q^{\prime}=\perp \vee\left(\Upsilon=p_{\circlearrowleft} \wedge Q^{\prime} \neq \circlearrowleft_{-}(-)\right)\right) \\ P_{\circlearrowleft}^{\prime} \|_{\left(\alpha, p_{\circlearrowleft}, q^{\prime}, p_{\circlearrowleft}\right)} Q^{\prime} \\ \text { if } P^{\prime}=\circlearrowleft_{p_{\circlearrowleft}}\left(P_{\circlearrowleft}^{\prime}\right) \\ \wedge Q^{\prime} \neq \perp \wedge Q^{\prime} \neq \circlearrowleft_{-}(-) \wedge \Upsilon \neq p_{\circlearrowleft} \\ \perp \quad \text { otherwise }\end{array}\right.$
where $\left(P^{\prime}, p^{\prime}, Q^{\prime}, q^{\prime}\right) \in\{(P, p, Q, q),(Q, q, P, p)\}$.
When one of the branches has been labeled as a loop, there are three options:
(i) The other branch is also a loop. In this case, the whole parallelism is marked as a loop labeled with its parent, and $\Upsilon$ is put to $\bullet$. (ii) Either it is a loop that has been unfolded without drawing any synchronization (this is known because $\Upsilon$ is equal to the parent of the loop), or the other branch already terminated (i.e., it is $\perp$ ). In this case, the parallelism is also marked as a loop, and the other branch is put to $\perp$ (this means that this process has been deadlocked). Also here, $\Upsilon$ is put to $\bullet$. (iii) If we are not in a loop, then we allow the parallelism to proceed by unlabeling the looped branch. In the rest of the cases $\perp$ is returned representing that this is a deadlock, and thus, stopping further computations.
(Synchronized Parallelism 5) This rule is used when the stack is empty. It basically analyzes the control and decides what are the applicable rules of the semantics. This is done with function AppRules which returns the set of rules $R$ that can be applied to a synchronized parallelism $P \|_{X} Q$ :

$$
\operatorname{AppRules}(P \| Q)= \begin{cases}\{\mathrm{SP} 1\} & \text { if } \tau \in \operatorname{FstEvs}(P) \\ \{\mathrm{SP} 2\} & \text { if } \tau \notin \operatorname{FstEvs}(P) \wedge \tau \in \operatorname{FstEvs}(Q) \\ R & \text { if } \tau \notin \operatorname{FstEvs}(P) \wedge \tau \notin \operatorname{FstEvs}(Q) \\ & \wedge R \neq \emptyset \\ \{\mathrm{SP} 4\} & \text { otherwise }\end{cases}
$$

where

$$
\begin{cases}\mathrm{SP} 1 \in R & \text { if } \exists e \in \operatorname{FstEvs}(P) \wedge e \notin X \\ \mathrm{SP} 2 \in R & \text { if } \exists e \in \operatorname{FstEvs}(Q) \wedge e \notin X \\ \mathrm{SP} 3 \in R & \text { if } \exists e \in \operatorname{FstEvs}(P) \\ & \wedge \exists e \in \operatorname{FstEvs}(Q) \wedge e \in X\end{cases}
$$

Essentially, AppRules decides what rules are applicable depending on the events that could happen in the next step. These events can be inferred by using function FstEvs. In particular, given a process $P$, function FstEvs returns the
set of events that can fire a rule in the semantics using $P$ as the control.

Therefore, rule (Synchronized Parallelism 5) prepares the stack allowing the semantics to proceed with the correct rule.
(Sequential Composition 1 and 2) Sequential Composition 1 is used to evolve process $P$ until it is finished or until it enters into an infinite loop. $P$ is evolved to $P^{\prime}$ which is put into the control. If $P$ is an infinite loop (this is detected thanks to the special constructor $\left.\circlearrowleft_{\varphi}\right)$, then the whole sequential composition is marked as a loop. Sequential Composition 2 is used when $P$ successfully finishes (it becomes $\Omega$ ). In this case, $Q$ is put into the control.
(SKIP and STOP) Whenever one of these rules is applied, the subcomputation finishes because $\Omega$ (for rule (SKIP)) and $\perp$ (for rule (STOP)) are put in the control, and these special constructors have no associated rule. As in previous rules, a node with the SKIP (respectively STOP) position is added to the graph.

We illustrate this semantics with some simple examples.
Example 3. Consider again the specification in Example 1. Due to the choice operator, in this specification two different events can occur, namely b and a. Therefore, Algorithm 1 obtains two computations, called respectively First iteration and Second iteration in Fig. 8. In this figure, for each state, we show a sequence of rules applied from left to right to obtain the next state. Here, for clarity, specification positions have been omitted from the control.

We first execute the semantics with the initial state $\left(\operatorname{MAIN}_{(\text {Main }, 0)}, \emptyset, \bullet,(\emptyset, \emptyset), \emptyset, \emptyset\right)$ and get the computation First iteration. This computation corresponds to the execution of the left branch of the choice (i.e., P) with the occurrence of event b. The final state is State $6=\left(\perp, G_{5}, 0,\left(\emptyset, S_{6}\right), \emptyset, \emptyset\right)$. Note that the stack $S_{6}$
contains a pair $(\mathrm{C} 1,\{\mathrm{C} 2\})$ to denote that the left branch of the choice has been executed.
Then, the algorithm calls function UpdStack and executes the semantics again with the new initial state State $7=\left(\operatorname{MAIN}_{(\operatorname{Main}, 0)}, G_{5}, \bullet,([(\mathrm{C} 2, \emptyset),(\mathrm{SP} 2, \emptyset)], \emptyset), \emptyset, \emptyset\right)$ and it gets the computation Second iteration. After this execution the final CSCFG $\left(G_{9}\right)$ has been computed. Figure 3 shows the CSCFG generated where white nodes were generated in the first iteration; and grey nodes were generated in the second iteration.

Next example 4 shows all computation steps executed by Algorithm 1 to obtain the CSCFG associated to the specification in Example 1.

Example 4 (Example revisited (computing step by step)). Consider again the specification in Example 1 and the computation of its CSCFG in Fig. 8. Each state of Fig. 8 has associated a sequence of rules whose execution is detailed in Fig. 9. In particular, this figure shows all computation steps executed by Algorithm 1 to obtain the CSCFG associated to this specification. Here, for clarity, specification positions have been omitted from the control and each computation step is labeled with the applied rule.

Example 5 (CSCFG generation in presence of loops). In this example we show a more interesting example where non-terminating processes appear.

Consider the following CSP specification where each literal has been labeled (in grey color) with its associated specification position.

$$
\begin{aligned}
& \text { MAIN }=\mathbf{a}_{(\text {Matn, 1.1) }} \rightarrow{ }_{(\text {MAIN, 1) }} \mathbf{a}_{\text {(MAIN, 1. 2.1) }} \rightarrow \text { (MAIN, 1.2 } \\
& \left.\operatorname{STOP}_{(\text {main, 1.2.2) }} \| \text { (Main, } \Lambda\right) \mathrm{P}_{(\text {main }, 2)} \\
& \text { \{a\} } \\
& \mathrm{P}=\mathrm{a}_{(\mathrm{P}, 1)} \rightarrow{ }_{(\mathrm{P}, \Lambda)} \mathrm{P}_{(\mathrm{P}, 2)}
\end{aligned}
$$

Following Algorithm 1, we use the initial state $\left(\operatorname{MAIN}_{(\operatorname{Main}, 0)}, \emptyset, \bullet,(\emptyset, \emptyset), \emptyset, \emptyset\right)$ to execute the semantics and get the computation of Fig. 13. This computation produces as a side effect the CSCFG shown in Fig. 11 for this specification. In this CSCFG, there is a loop edge between ( $\mathrm{P}, 2$ ) and (MAIN, 2). Note that the loop edge avoids infinite unfolding of the infinite process $P$, thus ensuring that the CSCFG is finite. Loop edges are introduced by the semantics whenever the context is repeated. In Fig. 13, when process $P$ is called a second time, rule (Process call) unfolds $P$, its right-hand side is marked as a loop and a loop edge between nodes 7 and 2 is added to the graph. In State 4, the looped process is in parallel with a process waiting to synchronize with it. In order to perform the synchronization, the loop is unlabeled (State 5) by rule (SP4). Later, it is labeled again by rule (Process Call) when the loop is repeated (State 8 in Fig. 13 (cont.)). Finally, rule (SP4) detects that the left branch of the parallelism is deadlocked and the parallelism is marked as a loop (State 9), thus finishing the computation.

| First iteration |  |
| :---: | :---: |
| State $0=\left(\operatorname{MAIN}_{(\text {Maiv }, 0)}, G_{0}, \bullet,(\emptyset, \emptyset), \emptyset, \emptyset\right)$ where $G_{0}=\emptyset$ | (Process Call) |
| State $1=\left((\mathrm{a} \rightarrow\right.$ STOP $\left.) \\|_{\{\mathrm{a}\}}((\operatorname{MAIN}, \Lambda), \bullet, \bullet \bullet)(\mathrm{P} \square(\mathrm{a} \rightarrow \operatorname{STOP})), G_{1}, 0,(\emptyset, \emptyset), \emptyset, \emptyset\right)$ where $G_{1}=G_{0}[0 \mapsto($ MAIN, 0$)]$ | (SP5)(SP2)(Choice) |
| $\begin{aligned} \text { State } 2=((\mathrm{a} \rightarrow & \text { STOP }) \\|_{\{\mathrm{a}\}}((\operatorname{MAIN}, \Lambda), \bullet, 2, \bullet) \\ & \text { Phe } \left., G_{2}, 0,\left(\emptyset, S_{2}\right), \emptyset, \emptyset\right) \\ & \text { where } G_{2}=G_{1}[1 \stackrel{0}{\mapsto}(\operatorname{MAIN}, \Lambda), 2 \stackrel{1}{\mapsto}(\text { MAIN }, 2)] \text { and } S_{2}=[(\mathrm{C} 1,\{\mathrm{C} 2\}),(\mathrm{SP} 2, \emptyset)] \end{aligned}$ | (SP5)(SP2)(Process Call) |
| $\begin{aligned} & \text { State } 3=((\mathrm{a} \rightarrow\mathrm{STOP}) \\ & \\|_{\{\mathrm{a} \mathrm{\}}\}}((\text { MAIN }, \Lambda), \bullet, 3, \bullet) \\ & \text { where } G_{3}=G_{2}\left[3 \stackrel{2}{\mapsto}(\mathrm{STOP}), G_{3}, 0,\left(\emptyset, S_{3}\right), \emptyset, \emptyset\right) \\ &\text { MAIN } 2.1)] \text { and } S_{3}=(\mathrm{SP} 2, \emptyset): S_{2} \end{aligned}$ | (SP5)(SP2)(Prefix) |
| $\begin{aligned} \text { State } 4=((\mathrm{a} \rightarrow & \text { STOP }) \\|_{\{\mathrm{a}\}}\left((\mathrm{MAIN}, \Lambda), \bullet, 5, \bullet \text { STOP, } G_{4}, 0,\left(\emptyset, S_{4}\right),\{4\}, \emptyset\right) \\ & \text { where } G_{4}=G_{3}[4 \stackrel{3}{\mapsto}(\mathrm{P}, 1), 5 \stackrel{4}{\mapsto}(\mathrm{P}, \Lambda)] \text { and } S_{4}=(\mathrm{SP} 2, \emptyset): S_{3} \end{aligned}$ | (SP5)(SP2)(STOP) |
| $\begin{aligned} \text { State } 5=((\mathrm{a} \rightarrow & \left.\mathrm{STOP}) \\|_{\{\mathrm{a} \mathrm{\}}}((\operatorname{MAIN}, \Lambda), \bullet, 6, \bullet) \perp, G_{5}, 0,\left(\emptyset, S_{5}\right), \emptyset, \emptyset\right) \\ & \quad \text { where } G_{5}=G_{4}[6 \stackrel{5}{\mapsto}(\mathrm{P}, 2)] \text { and } S_{5}=(\mathrm{SP} 2, \emptyset): S_{4} \end{aligned}$ | (SP5)(SP4) |
| $\begin{aligned} & \text { State } 6=\left(\perp, G_{5}, 0,\left(\emptyset, S_{6}\right), \emptyset, \emptyset\right) \\ & \quad \text { where } S_{6}=(\mathrm{SP} 4, \emptyset): S_{5}=[(\mathrm{SP} 4, \emptyset),(\mathrm{SP} 2, \emptyset),(\mathrm{SP} 2, \emptyset),(\mathrm{SP} 2, \emptyset),(\mathrm{C} 1,\{\mathrm{C} 2\}),(\mathrm{SP} 2, \emptyset)] \end{aligned}$ |  |

Fig. 7. An example of computation with Algorithm 1

## 5 Correctness

In this section we state the correctness of the proposed algorithm by showing that (i) the graph produced by the algorithm for a CSP specification $\mathcal{S}$ is the CSCFG

| Second iteration |  |
| :---: | :---: |
| State $7=\left(\operatorname{MAIN}_{(\text {Main }, 0)}, G_{5}, \bullet,\left(\operatorname{UpdStack}\left(S_{6}\right), \emptyset\right), \emptyset, \emptyset\right)=\left(\operatorname{MaIN}_{(\text {Main }, 0}, G_{5}, \bullet,([(C 2, \emptyset),(\operatorname{SP} 2, \emptyset)], \emptyset), \emptyset, \emptyset\right)$ | (Process Call) |
| State $\left.8=\left((\mathrm{a} \rightarrow \mathrm{STOP}) \\|_{\{\mathrm{a}\}}(\mathrm{MAIN}, \Lambda), \bullet, \bullet, \bullet\right)(\mathrm{P} \square(\mathrm{a} \rightarrow \mathrm{STOP})), G_{5}, 0,\left(S_{8}, \emptyset\right), \emptyset, \emptyset\right)$ where $S_{8}=[(\mathrm{C} 2, \emptyset),(\mathrm{SP} 2, \emptyset)]$ | (SP2)(Choice) |
| State $9=\left((\mathrm{a} \rightarrow\right.$ STOP $\left.) \\|_{\{\mathrm{a}\}}((\operatorname{MAIN}, \Lambda), \bullet, 2, \bullet)(\mathrm{a} \rightarrow \operatorname{STOP}), G_{5}, 0,\left(\emptyset, S_{9}\right), \emptyset, \emptyset\right)$ where $S_{9}=[(\mathrm{C} 2, \emptyset),(\mathrm{SP} 2, \emptyset)]$ | (SP5)(SP3)(Prefix)(Prefix) |
| $\begin{aligned} & \text { State } 10=\left(\operatorname{STOP} \\|_{\{\mathrm{a}\}}((\operatorname{MAIN}, \Lambda), 8,10, \bullet) \operatorname{STOP}, G_{6}, 0,\left(\emptyset, S_{10}\right),\{7,9\},\{7 \leftrightarrow 9\}\right) \\ & \quad \text { where } G_{6}=G_{5}[7 \stackrel{1}{\mapsto}(\text { MAIN }, 1.1), 8 \stackrel{7}{\mapsto}(\text { MAIN, } 1), 9 \stackrel{2}{\mapsto}(\text { MAIN, 2.2.1 }), 10 \stackrel{9}{\mapsto}(\text { MAIN, 2.2 })] \\ & \quad \text { and } S_{10}=(\mathrm{SP} 3, \emptyset): S_{9} \end{aligned}$ | (SP5)(SP1)(STOP) |
| $\begin{aligned} \text { State } 11=\left(\perp \\|_{\{a\}}(\operatorname{MAIN}, \Lambda), 11,10, \bullet\right) & \left.\mathrm{STOP}, G_{7}, 0,\left(\emptyset, S_{11}\right), \emptyset,\{7 \leftrightarrow 9\}\right) \\ & \quad \text { where } G_{7}=G_{6}[11 \stackrel{8}{\mapsto}(\text { MAIN }, 1.2)] \text { and } S_{11}=(\mathrm{SP} 1, \emptyset): S_{10} \end{aligned}$ | (SP5)(SP2)(STOP) |
| $\left.\begin{array}{rl} \text { State } 12=\left(\perp \\|_{\{\mathrm{a}\}}((\mathrm{MAIN}, \Lambda), 11,12, \bullet) \perp,\right. & \left.G_{8}, 0,\left(\emptyset, S_{12}\right), \emptyset,\{7 \leftrightarrow 9\}\right) \\ & \quad \text { where } G_{8}=G_{7}[12 \end{array} \stackrel{10}{\mapsto}(\text { MAIN }, 2.2 .2)\right] \text { and } S_{12}=(\mathrm{SP} 2, \emptyset): S_{11} 1$ | (SP5)(SP4) |
| $\begin{aligned} & \text { State } 13=\left(\perp, G_{8}, 0,\left(\emptyset, S_{13}\right), \emptyset,\{7 \leftrightarrow 9\}\right) \\ & \quad \text { where } S_{13}=(\mathrm{SP} 4, \emptyset): S_{12}=[(\mathrm{SP} 4, \emptyset),(\mathrm{SP} 2, \emptyset),(\mathrm{SP} 1, \emptyset),(\mathrm{SP} 3, \emptyset),(\mathrm{C} 2, \emptyset),(\mathrm{SP} 2, \emptyset)] \end{aligned}$ |  |
| State $\left.14=\left(\operatorname{MAIN}_{(\text {MAIN }, 0)}, G_{8}[7 \leftrightarrow 9], \bullet,\left(\operatorname{UpdStack}\left(S_{13}\right), \emptyset\right), \emptyset, \emptyset\right)=\left(\operatorname{MAIN}_{(\text {MAIN }, 0}\right), G_{9}, \bullet,(\emptyset, \emptyset), \emptyset, \emptyset\right)$ |  |

Fig. 8. An example of computation with Algorithm 1 (cont.)
of $\mathcal{S}$; and (ii) the algorithm terminates, even if non-terminating computations exist for the specification associated to the CSCFG. We need some preliminary definition and lemmas.

Fig. 9. An example of computation (step by step) with Algorithm 1

Definition 6. (Rewriting Step, Derivation) Given a state $s$ of the instrumented semantics, a rewriting step for $s\left(s \stackrel{\ominus}{\rightsquigarrow} s^{\prime}\right)$ is the application of a rule of the semantics with the occurrence of an event $e, \frac{\Theta}{s \xrightarrow{e} s^{\prime}}$ where $\Theta$ is a (possibly empty) set of rewriting steps. Given a state $s_{0}$, we say that the sequence $s_{0} \stackrel{\Theta_{0}}{\rightsquigarrow}$

|  |
| :---: |

Fig. 9. An example of computation (step by step) with Algorithm 1 (cont.)
$\ldots \stackrel{\Theta_{n}}{\rightsquigarrow} s_{n+1}, n \geq 0$, is a derivation of $s_{0}$ iff $\forall i, 0 \leq i \leq n, s_{i} \stackrel{\Theta_{i}}{\rightsquigarrow} s_{i+1}$ is a rewriting step. We say that the derivation is complete iff there is no possible rewriting step for $s_{n+1}$. We say that two derivations $\mathcal{D}, \mathcal{D}^{\prime}$ are equivalent (denoted $\mathcal{D} \equiv \mathcal{D}^{\prime}$ ) iff all specification positions in the control of a rewriting step of $\mathcal{D}$ also appear in a rewriting step of $\mathcal{D}^{\prime}$ and viceversa.


Fig. 9. An example of computation (step by step) with Algorithm 1 (cont.)

The following lemma ensures that all possible derivations of $\mathcal{S}$ are explored by Algorithm 1.


Fig. 10. An example of computation (step by step) with Algorithm 1 (cont.)

Lemma 1. Let $\mathcal{S}$ be a CSP specification and $\mathcal{D}$ a complete derivation of $\mathcal{S}$ performed with the standard semantics. Then, Algorithm 1 performs a derivation $\mathcal{D}^{\prime}$ such that $\mathcal{D} \equiv \mathcal{D}^{\prime}$.


Fig. 11. CSCFG of the program in Example 5

Proof. We prove first that the algorithm executes the instrumented semantics with a collection of initial states that explores all possible derivations. We prove this showing that every non-deterministic application of a rule is stored in the stack with all possible rules that can be applied; then, Algorithm 1 restarts the semantics with a new state that forces the semantics to explore a new derivation. This is done until all possible derivations have been explored.

Firstly, the standard semantics is deterministic except for two rules: (i) choice: the choice rules are evaluated until one branch is selected; and (ii) synchronized parallelism: the branches of the parallelism can be executed in any order.

In the case of choices, it is easy to see that the only applicable rule in the instrumented semantics is (Choice). Let us assume that we evaluate this rule with a pair of stacks $\left(S, S_{0}\right)$. There are two possibilities in this rule: If $S$ is empty, this rule puts in the control the left branch, and $[(\mathrm{C} 1,\{\mathrm{C} 2\})]$ is added to $S_{0}$, meaning that the left branch of the choice is executed and the right branch is pending. Therefore, we can ensure that the left branch is always explored because the algorithm evaluates the semantics with an initially empty stack. If the last element of $S$ is either ( $\mathrm{C} 1,\{\mathrm{C} 2\}$ ) or ( $\mathrm{C} 2, \emptyset)$, the semantics evaluates the first (resp. second) branch and deletes this element from $S$, and adds it to $S_{0}$.

We know that none of the other rules changes the stacks except (Synchronized Parallelism), and they both ((Synchronized Parallelism) and (Choice)) do it in the same manner. Therefore, we only have to ensure that the algorithm takes the stack $S_{0}$, selects another possibility (e.g., if C1 was selected in the previous evaluation, then $C 2$ is selected in the next evaluation, i.e., if the head of the stack is $(\mathrm{C} 1,\{\mathrm{C} 2\})$ it is changed to $(\mathrm{C} 2, \emptyset)$ ), puts it in the new initial state as the stack $S$, and the other stack is initialized for the next computation. This is exactly what the algorithm does by using function UpdStack.

In the case of synchronized parallelism, the semantics does exactly the same, but this case is a bit more complex because there are five different rules than can

|  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |

Fig. 12. Computation of the specification in Example 5 with the instrumented semantics

| $\quad($ Prefix $) \underset{\left(\mathrm{a} \rightarrow \mathrm{P}, G_{2}, 2,\left(\emptyset, S_{3}\right), \emptyset, \emptyset\right) \xrightarrow{\mathrm{a}}\left(\mathrm{P}, G_{2}[5 \stackrel{2}{\mapsto}(\mathrm{P}, 1), 6 \stackrel{5}{\mapsto}(\mathrm{P}, \Lambda)], 6,\left(\emptyset, S_{3}\right),\{5\}, \emptyset\right)}{ }$ and State $3=\left(\mathrm{a} \rightarrow \operatorname{STOP} \\|_{\{\mathrm{a} \mathrm{\}}\}}\left((\mathrm{MAIN}, \Lambda), 4,6, \bullet \mathrm{P}, G_{3}, 0,\left(\emptyset, S_{3}\right),\{3,5\},\{3 \leftrightarrow 5\}\right)\right.$ and $S_{3}=[(\mathrm{SP} 3, \emptyset),(\mathrm{SP} 2, \emptyset)]$ <br>  |
| :---: |

Fig. 12. Computation of the specification in Example 5 with the instrumented semantics (cont.)


Fig. 12. Computation of specification in Example 5 with the instrumented semantics (cont.)


Fig. 13. Computation of specification in Example 5 with the instrumented semantics (cont.)
both (Synchronized Parallelism 1) and (Synchronized Parallelism 2) can be executed with the same state. If this happens, the instrumented semantics executes one
rule first and then the other, and all the way around in the next evaluation. When a parallelism operator is in the control and the stack is empty, rule (Synchronized Parallelism 5) is executed. This rule uses function AppRules to determine what rules could be applied. If non-determinism exists in the standard semantics, it also exists in the instrumented semantics, because the control of both semantics is the same except for the following cases:

STOP Rule (STOP) of the instrumented semantics is not present in the standard semantics. When a STOP is reached in a derivation, the standard semantics stops the (sub)computation because no rule is applicable. In the instrumented semantics, when a STOP is reached in a derivation, the only rule applicable is (STOP) which performs $\tau$ and puts $\perp$ in the control. Then, the (sub)computation is stopped because no rule is applicable for $\perp$. Therefore, when the control in the derivation is STOP, the instrumented semantics performs one additional rewriting step with rule (STOP). Therefore, no additional non-determinism appears in the instrumented semantics due to (STOP).
$\perp$ This symbol only appears in the instrumented semantics. If it is in the control, the computation terminates because no rule can be applied. Therefore, no additional non-determinism appears in the instrumented semantics due to $\perp$.
$\circlearrowleft$ This symbol is introduced in the computation by (Process Call) or (Synchronized Parallelism 3). Once it is introduced, there are two possibilities: (i) it cannot be removed by any rule, thus this case is analogous to the previous one; or (ii) it is removed by (Synchronized Parallelism 4) because the $\circlearrowleft$ is the label of branch of a parallelism operator. In this case, the control remains the same as in the standard semantics, and hence, no additional non-determinism appears.

After (Synchronized Parallelism 5) has been executed, we have all possible applicable rules in the stack $S$, and $S_{0}$ remains unchanged. Then, the semantics executes the first rule, deletes it from $S$, and adds it to $S_{0}$. Therefore, the same mechanism used for choices is valid for parallelisms, and thus all branches of choices and parallelisms are explored.

Now, we have to prove that any possible (non-deterministic) derivation of MAIN with the standard semantics is also performed by the instrumented semantics as defined by Algorithm 1. We proof this lemma by induction on the length of the derivation $\mathcal{D}$.

In the base case, the initial state for the instrumented semantics induced by Algorithm 1 is in all cases $\left(\operatorname{MAIN}_{(\text {main }, 0)}, G, \bullet,(S, \emptyset), \emptyset, \emptyset\right)$ where $S=\emptyset$ in the first execution and $S \neq \emptyset$ in the other executions. Therefore, both semantics can only perform (Process Call) with an event $\tau$. Hence, in the base case, both derivations are equivalent. We assume as the induction hypothesis, that both derivations are equivalent after $n$ steps of the standard semantics, and we prove that they are also equivalent in the step $n+1$.

The most interesting cases are those in which the event is an external event. All possibilities are the following:

- (STOP) In this case, both derivations finish the computation. The instrumented semantics performs one step more with the application of rule (STOP) (see the first item in the previous description).
- (Process Call) and (Prefixing) In these cases, both derivations apply the same rule and the control is the same in both cases.
- (Internal Choice 1 and 2) In these cases, the control becomes the left (resp. right) branch. They are analogous to the (Choice) rule of the instrumented semantics because both branches will be explored in different derivations as proved before.
- (External Choice 1,2,3 and 4) With (External Choice 1 and 2) only $\tau$ events can be performed several times to evolve the branches of the choice. In every step the final control has the same specification position of the choice operator. Finally, one step is applied with (External Choice 3 or 4). Then, the set of rewriting steps performed with external choice are of the form:

$$
\frac{P_{0} \xrightarrow{\tau} P_{1}}{\left(P_{0} \square Q\right) \xrightarrow{\tau}\left(P_{1} \square Q\right)} \cdots \frac{P_{n} \stackrel{e}{\rightarrow} P_{n+1}}{\left(P_{n} \square Q\right) \xrightarrow{e} P_{n+1}}
$$

We can assume that (External Choice 1) is applied several times and finally (External Choice 3). This assumption is valid because (External Choice 2) is completely analogous to (External Choice 1); (External Choice 3) is completely analogous to (External Choice 4); and all combinations are going to be executed by the semantics as proved before. Then, we have an equivalent set of rewriting steps with the instrumented semantics:

$$
\overline{\left(P_{0} \square Q\right) \xrightarrow{\tau} P_{0}}, \overline{P_{0} \xrightarrow{\tau} P_{1}} \cdots \overline{P_{n} \xrightarrow{\tau} P_{n+1}}
$$

Clearly, in both sequences, the specification positions of the control are the same.

- (Synchronized Parallelism 1 and 2) Both rules can be applied interwound in the standard semantics. As it has been already demonstrated, we know that the same combination of rules will be applied by the instrumented semantics according to the algorithm use of the stack. The only difference is that the instrumented semantics performs an additional step with (Synchronized Parallelism 5), but this rule keeps the parallelism operator in the control; thus the specification position is the same and the claim holds.
- (Synchronized Parallelism 3) If this rule is applied in the standard semantics, in the instrumented semantics we apply (Synchronized Parallelism 5) and then (Synchronized Parallelism 3). The specification positions of the control do not change.

Lemma 2. Let $\mathcal{S}$ be a CSP specification, and $\mathcal{D}=s_{0} \stackrel{\Theta_{0}}{\rightsquigarrow} \ldots \stackrel{\Theta_{n}}{\rightsquigarrow} s_{n+1}$ a derivation of $\mathcal{S}$ performed with the instrumented semantics of Fig. 5. Then, $\forall$ rewriting step $s_{i} \stackrel{\Theta_{i}}{\rightsquigarrow} s_{i+1}, 0 \leq i<n$, with $s_{i}=\left(P_{\alpha}, G, m,\left(S, S_{0}\right), \Delta, \zeta\right)$, and $s_{i+1}=$ $\left(Q, G^{\prime}, n,\left(S^{\prime}, S_{0}^{\prime}\right), \Delta^{\prime}, \zeta^{\prime}\right)$; we have that $n \stackrel{m}{\mapsto} \alpha \in G^{\prime}$.

Proof. The lemma trivially holds for all rules of the semantics. The only interesting case is synchronized parallelism. In the case of (Synchronized Parallelism 1, 2 and 3), function InitBranch inserts $n \stackrel{m}{\mapsto} \alpha$ into $G^{\prime}$, the first time it is evaluated.

In the case of (Synchronized Parallelism 4), function LoopCheck returns another synchronized parallelism or a $\circlearrowleft$ only if one of the processes has been marked as a loop. This only happens if a process call has been unfolded; and in turn, this only happens if (Synchronized Parallelism 1, 2 or 3) has been performed. The other possibility is that function LoopCheck returns a $\perp$. In this case, $\perp$ cannot be further unfolded because no rule is applicable. Then, it must be the control of the sate $s_{n+1}$ and hence it is not required that $n \stackrel{m}{\mapsto} \alpha \in G^{\prime}$. Finally, (Synchronized Parallelism 5) starts a subderivation with a parallelism operator in the control and a non-empty stack. Therefore, another of the previous rules must be applied after it, and thus, the claim follows.

Lemma 3. Let $\mathcal{S}$ be a CSP specification, and $G=\left(N, E_{c}, E_{l}, E_{s}\right)$ the graph produced for $\mathcal{S}$ by Algorithm 1. Then, for each two nodes $n, n^{\prime} \in N,\left(n \mapsto n^{\prime}\right) \in$ $E_{c}$ iff the control can pass from $l(n)$ to $l\left(n^{\prime}\right)$ and $\nexists n^{\prime \prime} .\left(n \mapsto n^{\prime \prime}\right) \notin E_{c}$ and $\left(n^{\prime \prime} \mapsto n^{\prime}\right) \notin E_{c}$.

Proof. The fact that $\exists n^{\prime \prime} .\left(n \mapsto n^{\prime \prime}\right) \notin E_{c}$ and $\left(n^{\prime \prime} \mapsto n^{\prime}\right) \notin E_{c}$ implies that the control can pass from $n$ to $n^{\prime}$ directly, i.e., without a transitive relation. This condition is needed because the CSCFG only contains control-flow edges between those nodes where the control can pass from one to the other directly. Moreover, all the arcs in $E_{c}$ are added to $G$ by the instrumented semantics. Therefore, we only have to proof that in every derivation $\mathcal{D}$ of the semantics, for every new $\operatorname{arc}\left(n \mapsto n^{\prime}\right)$ added to $E_{c}$, the control can pass from $l(n)$ to $l\left(n^{\prime}\right)$. We proof this lemma by induction on the length of the derivation $\mathcal{D}$. The base case starts with the initial state $\left(\operatorname{MAIN}_{(\operatorname{main}, 0)}, \emptyset, \bullet,(\emptyset, \emptyset), \emptyset, \emptyset\right)$. Therefore the only rule applicable is (Process Call). This case is trivial because in the new arc $n \stackrel{m}{\mapsto} \alpha$, $l(m)=(\operatorname{MAIN}, 0)$ and $l(n)=(\operatorname{maIN}, \Lambda)$. Hence, by item (i) of Definition 2 we have that the control can pass from $l(m)$ to $l(n)$. We assume as the induction hypothesis that the lemma holds in the $i$ first rewriting steps of $\mathcal{D}$, and we prove that it also holds in the step $i+1$. In the rewriting step $i+1$, one of the following rules must be applied:

- (Process Call) This case is analogous to the base case, because in the new added arc $n \stackrel{m}{\mapsto} \alpha, m$ must be the name of a process $N$, and $n=(\mathrm{N}, \Lambda)$. Therefore, by item (i) of Definition 2 we have that the control can pass from $l(m)$ to $l(n)$.
- (Prefixing) Two new arcs are added to $G . n \stackrel{m}{\mapsto} \alpha$ and $o \stackrel{n}{\mapsto} \beta$. Trivially, the control can pass from $l(n)$ to $l(o)$ by item (iii) of Definition 2. Moreover, by Lemma 2 we have that a node with the specification position of $P$ and parent $o$ will be added to $G$ in the next rewriting step. Therefore, the control can pass from $l(o)$ to $\mathcal{P o s}(P)$ by item (iv) of Definition 2.
- (Choice) One of the branches $P^{\prime}$ is the new control. Therefore, by Lemma 2 we have that a node with the specification position of $P^{\prime}$ and parent $n$ will be added to $G$ in the next rewriting step. Thus, the control can pass from $l(n)$ to the next fresh reference by item (ii) of Definition 2.
- (Synchronized Parallelism 1 and 2) They are analogous to the case of the choice.
- (Synchronized Parallelism 3) In this case, two new nodes are added. Each of them corresponds to one branch and are exactly the same as in (Synchronized Parallelism 1 and 2).
- (Synchronized Parallelism 4) This rule does not add new nodes to the graph.
- (Synchronized Parallelism 5) This rule starts a subderivation by applying one of the other rules associated to synchronized parallelism, thus the claim follows by the induction hypothesis.

Lemma 4. Let $\mathcal{S}$ be a CSP specification, $\mathcal{D}$ a derivation of $\mathcal{S}$ performed with the instrumented semantics, and $G=\left(N, E_{c}, E_{l}, E_{s}\right)$ the graph produced by $\mathcal{D}$. Then, there exists a synchronization edge $\left(a \leftrightarrow a^{\prime}\right) \in E_{s}$ for each synchronization in $\mathcal{D}$ where $a$ and $a^{\prime}$ are the nodes of the synchronized events.

Proof. After every execution of the semantics, Algorithm 1 introduces in the graph $G$ all the synchronizations in the set $\zeta$. Therefore, we have to prove that at the end of the derivation $\mathcal{D}$, all the synchronizations are in $\zeta$.

We prove this lemma by induction on the length of the derivation $\mathcal{D}=s_{0} \stackrel{\Theta_{0}}{\rightsquigarrow}$ $s_{1} \stackrel{\Theta_{1}}{\rightsquigarrow} \ldots \stackrel{\Theta_{n}}{\rightsquigarrow} s_{n+1}$. We can assume that the derivation starts with the initial state $\left(\operatorname{MAIN}_{(\operatorname{main}, 0)}, \emptyset, \bullet,(\emptyset, \emptyset), \emptyset, \emptyset\right)$, thus in the base case, the only rule applicable is (Process Call) and hence no synchronization is possible. We assume as the induction hypothesis that there exists a synchronization edge $\left(a \leftrightarrow a^{\prime}\right) \in E_{s}$ for each synchronization in $s_{0} \xrightarrow{\Theta_{0}} \ldots \stackrel{\Theta_{i-1}}{\leadsto} s_{i}$ with $0<i \leq n$ and prove that the lemma also holds for the next rewriting step $s_{i} \stackrel{\Theta_{i}}{\sim} s_{i+1}$.

Firstly, only (Synchronized Parallelism 3) allows the synchronization of events. Therefore, $\left(a \leftrightarrow a^{\prime}\right) \in \zeta$ only if the control of $s_{i}, P$, is a synchronized parallelism, or if a (Synchronized Parallelism 3) is applied in $\Theta_{i}$. Then, let us consider the case where $\stackrel{\Theta_{i}}{\rightsquigarrow}$ is the application of rule (Synchronized Parallelism 3). This proof is also valid to the case where (Synchronized Parallelism 3) is applied in $\Theta_{i}$. We have the following rewriting step:

```
\(\frac{\text { Left } \quad \text { Right }}{\left(P 1 \|_{X}\left(\alpha, n_{1}, n_{2}, r\right) P 2, G, m,\left(S^{\prime}:(\mathrm{SP} 3, \text { rules }), S_{0}\right),-, \zeta\right)} \quad e \in X\)
    \(\xrightarrow{e}\left(P^{\prime}, G^{\prime \prime}, m,\left(S^{\prime \prime \prime}, S_{0}^{\prime \prime}\right), \Delta_{1} \cup \Delta_{2}\right.\),
        \(\left.\zeta^{\prime \prime} \cup\left\{s_{1} \leftrightarrow s_{2} \mid s_{1} \in \Delta_{1} \wedge s_{2} \in \Delta_{2}\right\}\right)\)
\(\left(G_{1}^{\prime}, n_{1}^{\prime}\right)=\operatorname{InitBranch}\left(G, n_{1}, m, \alpha\right) \wedge\)
Left \(=\left(P 1, G_{1}^{\prime}, n_{1}^{\prime},\left(S^{\prime},(\mathrm{SP} 3\right.\right.\), rules \(\left.\left.): S_{0}\right),{ }_{-}, \zeta\right)\)
    \(\xrightarrow{e}\left(P 1^{\prime}, G_{1}^{\prime \prime}, n_{1}^{\prime \prime},\left(S^{\prime \prime}, S_{0}^{\prime}\right), \Delta_{1}, \zeta^{\prime}\right) \wedge\)
\(\left(G_{2}^{\prime}, n_{2}^{\prime}\right)=\operatorname{InitBranch}\left(G_{1}^{\prime \prime}, n_{2}, m, \alpha\right) \wedge\)
Right \(=\left(P 2, G_{2}^{\prime}, n_{2}^{\prime},\left(S^{\prime \prime}, S_{0}^{\prime}\right), ~, ~, \zeta^{\prime}\right)\)
        \(\xrightarrow{e}\left(P 2^{\prime}, G^{\prime \prime}, n_{2}^{\prime \prime},\left(S^{\prime \prime \prime}, S_{0}^{\prime \prime}\right), \Delta_{2}, \zeta^{\prime \prime}\right) \wedge\)
```


Because (Prefixing) is the only rule that performs an $a$ event without further conditions, we know that $P 1$ must be a prefixing operator or a parallelism containing a prefixing operator whose prefix is $a$, i.e., we know that the rule applied in Left is fired with an event $a$; and we know that all the rules of the semantics except (Prefixing) need to fire another rule with an event $a$ as a condition. Therefore, at the top of the condition rules, there must be a (Prefixing). The same happens with $P 2$. Hence, two prefixing rules (one for $P 1$ and one for $P 2$ ) have been fired as a condition of this rule.

In addition, the new set $\zeta$ contains the synchronization set $\left\{s_{1} \leftrightarrow s_{2} \mid s_{1} \in\right.$ $\left.\Delta_{1} \wedge s_{2} \in \Delta_{2}\right\}$ where $\Delta_{1}$ and $\Delta_{2}$ are the sets of references to the events that must synchronize in Left and Right, respectively.

Hence, we have to prove that all and only the events (a) that must synchronize in Left are in $\Delta_{1}$. We prove this by showing that all references to the synchronized events are propagated down by all rules from the (Prefixing) in the top to the (Synchronized Parallelism 3). And the proof is analogous for Right.

The only applicable rules in

$$
\begin{aligned}
&\left(P 1, G_{1}^{\prime}, n_{1}^{\prime},\left(S^{\prime}:(\mathrm{SP} 3, \text { rules }), S_{0}\right), \_, \zeta\right) \\
& e \\
& e \\
&\left(P 1^{\prime}, G_{1}^{\prime \prime}, n_{1}^{\prime \prime},\left(S^{\prime \prime}, S_{0}^{\prime}\right), \Delta_{1}, \zeta^{\prime}\right)
\end{aligned}
$$

are:

- (Prefixing) In this case, the prefix $a$ is added to $\Delta_{1}$.
- (Synchronized Parallelism 1, 2 and 5) In these cases, the set $\Delta$ is propagated down.
- (Synchronized Parallelism 3) In this case, the sets $\Delta_{1}$ and $\Delta_{2}$ are joined and propagated down.

Therefore, all the synchronized events are in the set $\Delta_{1}$ and the claim follows.
Lemma 5. Let $\mathcal{S}$ be a CSP specification and $G=\left(N, E_{c}, E_{l}, E_{s}\right)$ the CSCFG produced by Algorithm 1 for $\mathcal{S}$. Then, $\left(n_{1} \rightsquigarrow n_{2}\right) \in E_{l}$ iff $l\left(n_{1}\right)$ and $l\left(n_{2}\right)$ are process calls that refer to the same process $M \in \mathcal{N}$ and $n_{2} \in \mathcal{C o n}\left(n_{1}\right)$.

Proof. First, all edges in $E_{l}$ are introduced in a derivation $\mathcal{D}$ of the instrumented semantics. Let $\mathcal{D}=s_{0} \stackrel{\Theta_{0}}{\leadsto} \ldots \stackrel{\Theta_{n}}{\leadsto} s_{n+1}$ a derivation that introduced $\left(n_{1} \rightsquigarrow n_{2}\right)$ into $E_{l}$. Then, this arc is necessarily introduced in a rewriting step where rule (Process Call) was applied, because this is the only rule that adds arcs to $E_{l}$. In rule (Process Call), arcs are added by means of function LoopCheck. An arc is added to $E_{l}$ if and only if $\exists n_{2} \in \operatorname{Path}\left(0, n_{1}\right) \wedge n_{2} \stackrel{t}{\mapsto} M \in E_{c}$, where $n_{1}$ is the reference of the current node added to $N$. Therefore, because function LoopCheck adds the arc $n_{1} \rightsquigarrow n_{2}$, then $l\left(n_{1}\right)=l\left(n_{2}\right)=M$. Hence, we need to prove that $n_{2} \in \mathcal{C} \operatorname{on}\left(n_{1}\right)$.

First, by Lemma 3, if the control can pass (transitively) from $n_{2}$ to $n_{1}$, then we have in $G$ a path of control edges $n_{2} \mapsto^{*} n_{1}$. We can show that this path is loop-free by contradiction. Let us consider that the path is not loop-free. Then, $n_{2} \mapsto^{*} n_{3} \mapsto^{*} n_{1}$ with $l\left(n_{3}\right)=M$ and $n_{1} \neq n_{3}$. The derivation $\mathcal{D}$ must be of the form:

$$
\mathcal{D}=s_{0} \stackrel{\Theta_{0}}{\rightsquigarrow} \ldots s_{i} \stackrel{\Theta_{i}}{\rightsquigarrow} s_{i+1} \ldots \stackrel{\Theta_{n}}{\rightsquigarrow} s_{n+1}, 0<i \leq n
$$

where the rewriting step $s_{i} \stackrel{\Theta_{i}}{\rightsquigarrow} s_{i+1}$ introduced $n_{3}$ in $G$. Clearly, $n_{3}$ is necessarily introduced in $G$ by rule (Process Call) which is the only rule that adds a process call to the graph. Moreover, by the definition of LoopCheck and because $\exists n_{2} \cdot n_{2} \stackrel{t}{\mapsto} M \in G \wedge n_{2} \in \operatorname{Path}\left(0, n_{3}\right)$, we know that the control of $s_{i+1}$ is $\circlearrowleft_{n_{2}}(r h s(N))$. But this is a contradiction with the fact that $n_{3} \mapsto^{*} n_{1}$ because no rule of the semantics adds a control-flow edge of the form $n_{3} \mapsto$. In particular, once the control of $s_{i+1}$ is labeled with $\circlearrowleft_{n_{2}}$, only the rule (Synchronized Parallelism 4) can remove the label of the control. This is done with function LoopControl in the third case of the definition. But in this case, the parallelism is marked as $P_{\circlearrowleft}^{\prime} \|_{X}\left(\alpha, p_{\circlearrowleft}, q^{\prime}, p_{\circlearrowleft}\right) Q^{\prime}$ where $p_{\circlearrowleft}$ is the label of the previous process call to $M$. Hence, $p_{\circlearrowleft}=n_{2}$; and thus, the next control edges added to $G$ start from $n_{2}$, and not from $n_{3}$.

Theorem 1 (Correctness) Let $\mathcal{S}$ be a CSP specification and $G$ the graph produced for $\mathcal{S}$ by Algorithm 1. Then, $G$ is the CSCFG associated to $\mathcal{S}$.

Proof. In order to prove that $G$ is a CSCFG, we need to prove that it satisfies the properties of Definition 5 . Let us consider a CSCFG $G=\left(N, E_{c}, E_{l}, E_{s}\right)$.

Firstly, by Lemma 3, and because control-flow is a transitive relation, we know that for each rewriting step in a derivation of $\mathcal{S}$ the control can pass from MAIN to the positions added to $N$. Hence, $\forall n \in N$. $l(n) \in \mathcal{P o s}(\mathcal{S})$ and $l(n)$ is executable in $\mathcal{S}$. In addition, we have that:

- by Lemma 3, for each two nodes $n, n^{\prime} \in N,\left(n \mapsto n^{\prime}\right) \in E_{c}$ iff the control can pass from $n$ to $n^{\prime}$.
- by Lemma $5,\left(n_{1} \rightsquigarrow n_{2}\right) \in E_{l}$ iff $l\left(n_{1}\right)$ and $l\left(n_{2}\right)$ are (possibly different) process calls that refer to the same process $M \in \mathcal{N}$ and $n_{2} \in \mathcal{C}$ on $\left(n_{1}\right)$;
- by Lemma 4, there exists a synchronization edge ( $a \leftrightarrow a^{\prime}$ ) in $G$ for each synchronization in a derivation $\mathcal{D}$ of $\mathcal{S}$ where $a$ and $a^{\prime}$ are the nodes of the synchronized events. And, by Lemma 1 we know that all possible derivations of $\mathcal{S}$ are explored by Algorithm 1.

Moreover, we know that the only nodes in $N$ are the nodes induced by $E_{c}$ because all the nodes added to $G$ are added by connecting the new node to the last added node (i.e., if the current reference is $m$ and the new fresh reference is $n$, then the new node is always added as $G[n \stackrel{m}{\mapsto} \alpha])$. Hence, all nodes are related by control edges and thus the claim holds.

Theorem 2 (Termination) Let $\mathcal{S}$ be a CSP specification. Then, the execution of Algorithm 1 with $\mathcal{S}$ terminates.

Proof. In order to prove that the algorithm terminates we have to show that the stack never grows infinitely. For this purpose, we have to prove that all executions of the semantics terminate. This is sufficient because function UpdStack, which is the only one that also manipulates the stack, always either reduces its size or leaves it unchanged. So, as the stack is always increased by rule (Synchronized Parallelism 5) or by rule (Choice), we have to show that there is not any derivation which fires these rules infinitely. We use a function over sets of rewriting steps which is defined as follows:

$$
\left.[\mathcal{R}]=\bigcup\left\{s \stackrel{\ominus}{\rightsquigarrow} s^{\prime}\right\} \cup[\Theta] \mid s \stackrel{\Theta}{\rightsquigarrow} s^{\prime} \in \mathcal{R}\right\}
$$

Given a set $\mathcal{R}$ of rewriting steps, it returns $\mathcal{R}$ and all the rewriting steps included in the subderivations of $\mathcal{R}$.

In the following, we will consider derivations where the state is simplified and only the control is taken into account. In order to prove that there does not exist any infinite derivation, we consider the main derivation $\mathcal{D}$ of the semantics where the initial control is MAIN. If $\forall s_{i} \stackrel{\Theta_{i}}{\leadsto} s_{i+1} \in \mathcal{D}$ where $\nexists s \stackrel{\ominus}{\leadsto} s^{\prime} \in\left[\left\{s_{i} \stackrel{\Theta_{i}}{\sim} s_{i+1}\right\}\right]$ such that $s=N$ and $s^{\prime}=\circlearrowleft(r h s(N))$, then we know that the derivation $\mathcal{D}$ is finite because no infinite unfolding is possible (we know that no process is called twice) and the specification is finite. Hence, as the application of the rules of the semantics always reduces the size of the process in the control, it will eventually terminate with $\perp$.

The other case happens when the same process appears twice in a derivation. We can assume that, after a number of rewriting steps, we find the first occurrence of a rewriting step $s_{i} \stackrel{\Theta_{i}}{\rightsquigarrow} s_{i+1} \in \mathcal{D}$ where $\exists s \stackrel{\ominus}{\rightsquigarrow} s^{\prime} \in\left[\left\{s_{i} \stackrel{\Theta_{i}}{\rightsquigarrow} s_{i+1}\right\}\right]$ such that $s=N$ and $s^{\prime}=\circlearrowleft(r h s(N))$. When this happens, we know that $N$ has been already unfolded in a previous rewriting step, and function LoopCheck introduces the loop $s^{\prime}$ through the rule (Process Call) which corresponds to $s \stackrel{\ominus}{\rightsquigarrow} s^{\prime}$.

We have two possibilities: the first one happens when $s^{\prime}=s_{i+1}$ which means that this is the last rewriting step of derivation $\mathcal{D}$ since there does not exist any rule for $\circlearrowleft(-)$. In the other case, when $s^{\prime} \neq s_{i+1}$, we have that rewriting step $s_{i} \stackrel{\Theta_{i}}{\rightsquigarrow} s_{i+1}$ corresponds to the application of rule (Synchronized Parallelism 1), (Synchronized Parallelism 2) or (Synchronized Parallelism 5), since no other rule can fire the rule (Process Call) into the associated $\Theta_{i}$. Note that rule (Synchronized Parallelism 3) can not be applied here because event $\tau$ can not fire this rule. This means that $s_{i}$ is a parallelism which has nested parallelisms in its branches and some of these branches has the process call $N$. Then we know that $\exists\left(s \|_{X} P\right) \xrightarrow{\Theta^{\prime}}$ $\left(s^{\prime} \|_{X} P\right) \in\left[\left\{s_{i} \stackrel{\Theta_{i}}{\rightsquigarrow} s_{i+1}\right\}\right]$ where $\Theta^{\prime}=\left\{s \stackrel{\ominus}{\rightsquigarrow} s^{\prime}\right\}^{3}$. Now, process $P$ could be of one of these kinds:

[^3]- $\perp$ : In this case, there is a rewriting step $s_{j}{ }^{\Theta_{j}} s_{j+1} \in \mathcal{D}$ with $j>i$ such that $\left(s_{X}^{\prime} \| \perp\right) \stackrel{\Theta_{\rightsquigarrow}^{\prime \prime}}{\circlearrowleft}\left(s^{\prime} \|_{X} \perp\right) \in\left[\left\{s_{j} \stackrel{\Theta_{j}}{\rightsquigarrow} s_{j+1}\right\}\right]$ by application of rule (Synchronized Parallelism 4). Then, if $s_{j+1}=\circlearrowleft\left(s_{X}^{\prime} \| \perp\right)$ then the computation terminates. Else, $s_{j+1}$ is a parallelism and it terminates by induction.
$-\circlearrowleft\left(Q^{\prime}\right)$ : In this case, there is a rewriting step $s_{j} \stackrel{\Theta_{j}}{\rightsquigarrow} s_{j+1} \in \mathcal{D}$ with $j>i$
 (Synchronized Parallelism 4). Then, if $s_{j+1}=\circlearrowleft\left(s^{\prime} \| Q^{\prime}\right)$ then the computation terminates. Else, $s_{j+1}$ is a parallelism and it terminates by induction.
- STOP: Then, there is some rewriting step $s_{j} \stackrel{\Theta_{j}}{\rightsquigarrow} s_{j+1} \in \mathcal{D}$ with $j>i$ such that $\left(s_{X}^{\prime} \|_{X}\right.$ STOP $) \stackrel{\Theta_{\rightsquigarrow}^{\prime \prime}}{\underset{\sim}{\prime}}\left(s^{\prime} \| \perp\right) \in\left[\left\{s_{j} \stackrel{\Theta_{j}}{\rightsquigarrow} s_{j+1}\right\}\right]$, and it terminates by case $\perp$.
$-a \rightarrow Q$ : Then, there are two possibilities. If $a \notin X$ then there is some rewriting step $s_{j} \stackrel{\theta_{j}}{\rightsquigarrow} s_{j+1} \in \mathcal{D}$ with $j>i$ such that $\left(s_{X}^{\prime} \|_{X}(a \rightarrow Q)\right) \stackrel{\theta^{\prime \prime}}{\rightsquigarrow}\left(s^{\prime} \| Q\right) \in$ $\left[\left\{s_{j} \stackrel{\Theta_{j}}{\sim} s_{j+1}\right\}\right]$, then it terminates by induction. Else, when $a \in X$, there is a rewriting step $s_{j} \stackrel{\Theta_{j}}{\rightsquigarrow} s_{j+1} \in \mathcal{D}$ with $j>i$ such that $\left(s^{\prime} \|_{X}(a \rightarrow Q)\right) \xrightarrow{\Theta_{\rightsquigarrow}^{\prime \prime}}$ $(r h s(N) \underset{X}{\|} a \rightarrow Q) \in\left[\left\{s_{j} \stackrel{\theta_{j}}{\rightsquigarrow} s_{j+1}\right\}\right]$, where parallelism's $\Upsilon$ is equal to the label of the loop. Then, we have again two options. The first one is that some synchronization is drawn before to have $N$ again in the left branch of the parallelism. Then, we have a rewriting step $s_{k} \stackrel{\Theta_{k}}{\leadsto} s_{k+1} \in \mathcal{D}$ with $k>j$ such that $\left(s^{\prime \prime} \underset{X}{\|} \rightarrow Q\right) \stackrel{\Theta_{\rightsquigarrow}^{\prime \prime \prime}}{\rightsquigarrow}\left(s^{\prime \prime \prime} \|_{X} Q\right) \in\left[\left\{s_{k} \stackrel{\Theta_{k}}{\rightsquigarrow} s_{k+1}\right\}\right]$, and $\Upsilon$ is put to - if the synchronization is not included in $\zeta$ yet. This case terminates, by induction hypothesis. Otherwise, if the synchronization was in $\zeta$, then $\left(s^{\prime \prime} \| a \rightarrow Q\right) \stackrel{\Theta^{\prime \prime \prime}}{\rightsquigarrow} \circlearrowleft\left(s^{\prime \prime \prime} \| Q\right) \in\left[\left\{s_{k} \stackrel{\Theta_{k}}{\rightsquigarrow} s_{k+1}\right\}\right]$ by rule (Synchronized Parallelism 3). If $s_{k+1}=\circlearrowleft\left(s^{\prime \prime \prime} \| Q\right)$, the derivation has terminated, else termination is proved by induction. The second case is when none synchronization is drawn before to have $N$ again into the left branch. In this case, we have that $s_{k} \xrightarrow{\Theta_{k}}$ $s_{k+1} \in \mathcal{D}$ with $k>j$ such that $\left(s^{\prime} \| a \rightarrow Q\right) \stackrel{\Theta_{X}^{\prime \prime \prime}}{\sim} \underset{\sim}{\circlearrowleft}\left(s^{\prime} \|_{X} \perp\right) \in\left[\left\{s_{k} \stackrel{\Theta_{k}}{\rightsquigarrow} s_{k+1}\right\}\right]$ by rule (Synchronized Parallelism 4). If $s_{k+1}=\circlearrowleft\left(s_{X}^{\prime} \| \perp\right)$, the derivation has terminated, else, termination is proved by induction.
$-Q_{1} \square Q_{2}$ : In this case, one of the branches is selected, and independently of which one is followed, the computation terminates by induction. Then, there is some rewriting step $s_{j} \stackrel{\Theta_{j}}{\leadsto} s_{j+1} \in \mathcal{D}$ with $j>i$ such that $\left(s_{X}^{\prime} \|\left(Q_{1} \square Q_{2}\right)\right) \stackrel{\Theta^{\prime \prime}}{\rightsquigarrow}$

- $Q_{1} \|_{Y} Q_{2}$ : Using the induction hypothesis, a parallelism always terminates, so we have to consider that in this case $Q$ will be rewritten either to $\perp$ or to $\circlearrowleft\left(Q_{1}^{\prime} \|_{Y} Q_{2}^{\prime}\right)$.


## 6 Conclusions

This work introduces an algorithm to build the CSCFG associated to a CSP specification. The algorithm uses an instrumentation of the standard CSP's operational semantics to explore all possible computations of a specification. The semantics is deterministic because the rule applied in every step is predetermined by the initial configuration. Therefore, the algorithm can execute the semantics several times to iteratively explore all computations and hence, generate the whole CSCFG. The CSCFG is generated even for non-terminating specifications due to the use of a loop detection mechanism controlled by the semantics. This semantics is an interesting result because it can serve as a reference mark to prove properties such as completeness of static analyses based on the CSCFG. The way in which the semantics has been instrumented can be used for other similar purposes with slight modifications. For instance, the same design could be used to generate other graph representations of a computation such as Petri nets.

On the practical side, we have implemented a tool called SOC [7] which is able to automatically generate the CSCFG of a CSP specification. The CSCFG is later used for debugging and program simplification. SOC has been integrated into the most extended CSP animator and model-checker ProB [1,5], that shows the maturity and usefulness of this tool and of CSCFGs. The last release of $S O C$ implements the algorithm described in this paper. However, in the implementation the algorithm is much more complex because it contains some improvements that significantly speed up the CSCFG construction. The most important improvement is to avoid repeated computations. This is done by: (i) state memorization: once a state already explored is reached the algorithm stops this computation and starts with another one; and (ii) skipping already performed computations: computations do not start from MAIN, they start from the next non-deterministic state in the execution (this is provided by the information of the stack).

The implementation, source code and several examples are publicly available at:
http://users.dsic.upv.es/ jsilva/soc/

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[^1]:    ${ }^{1}$ We assume that fresh references are numeric and generated incrementally.

[^2]:    ${ }^{2}$ Only once because it will be labeled again by rule (Process Call) when the loop is repeated. In Section 4, we present an example with loops where this situation happens.

[^3]:    ${ }^{3}$ Of course, $s$ could be on the right branch of the parallelism, but we only consider this case since the other one is analogous.

